

**2007 0425**

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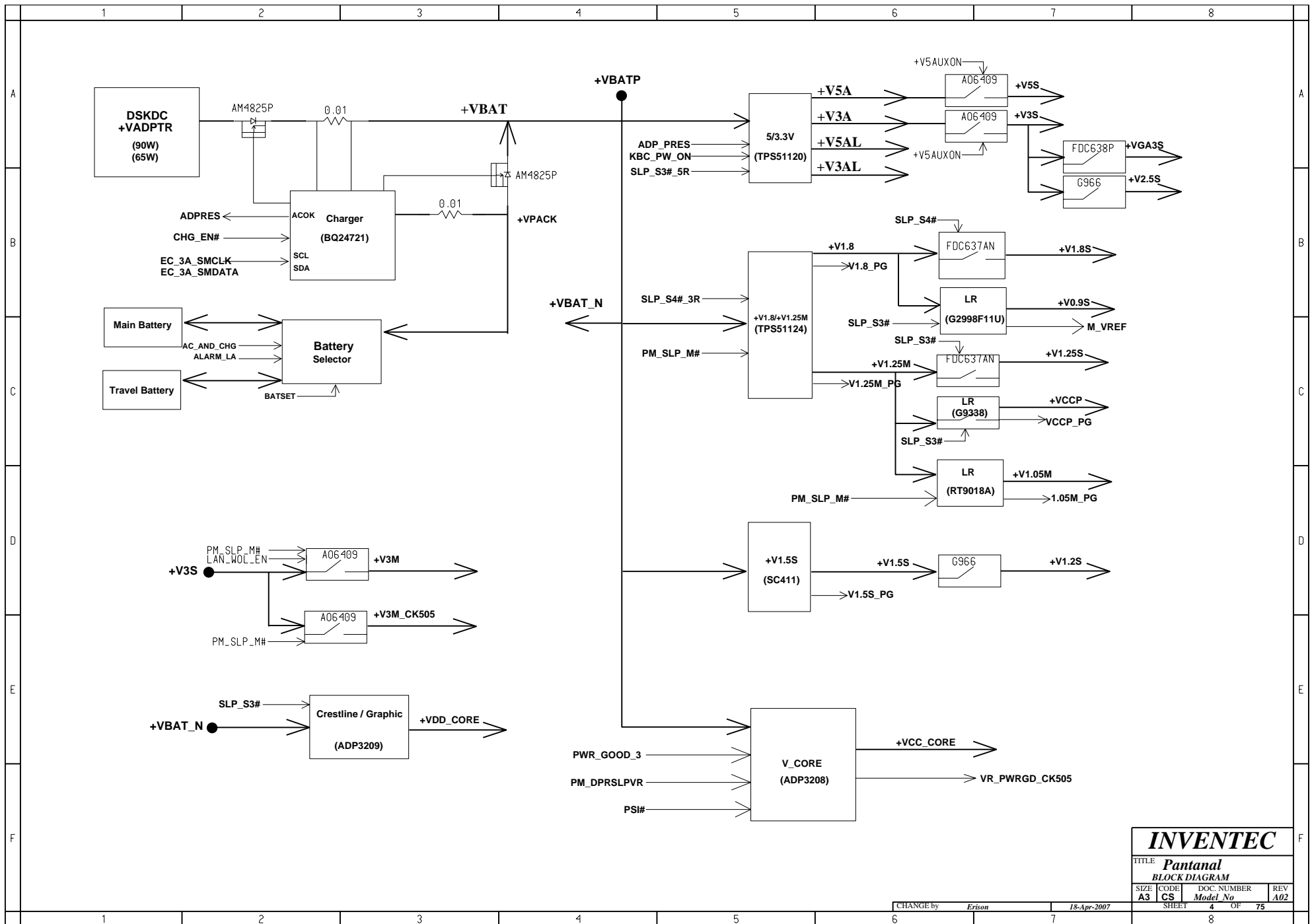
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**INVENTEC**

TITLE <i>Pantanal</i>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <i>Model No</i>	REV <b>A02</b>
SHEET <b>2</b>		OF <b>75</b>	

CHANGE by *Erison* 18-Apr-2007

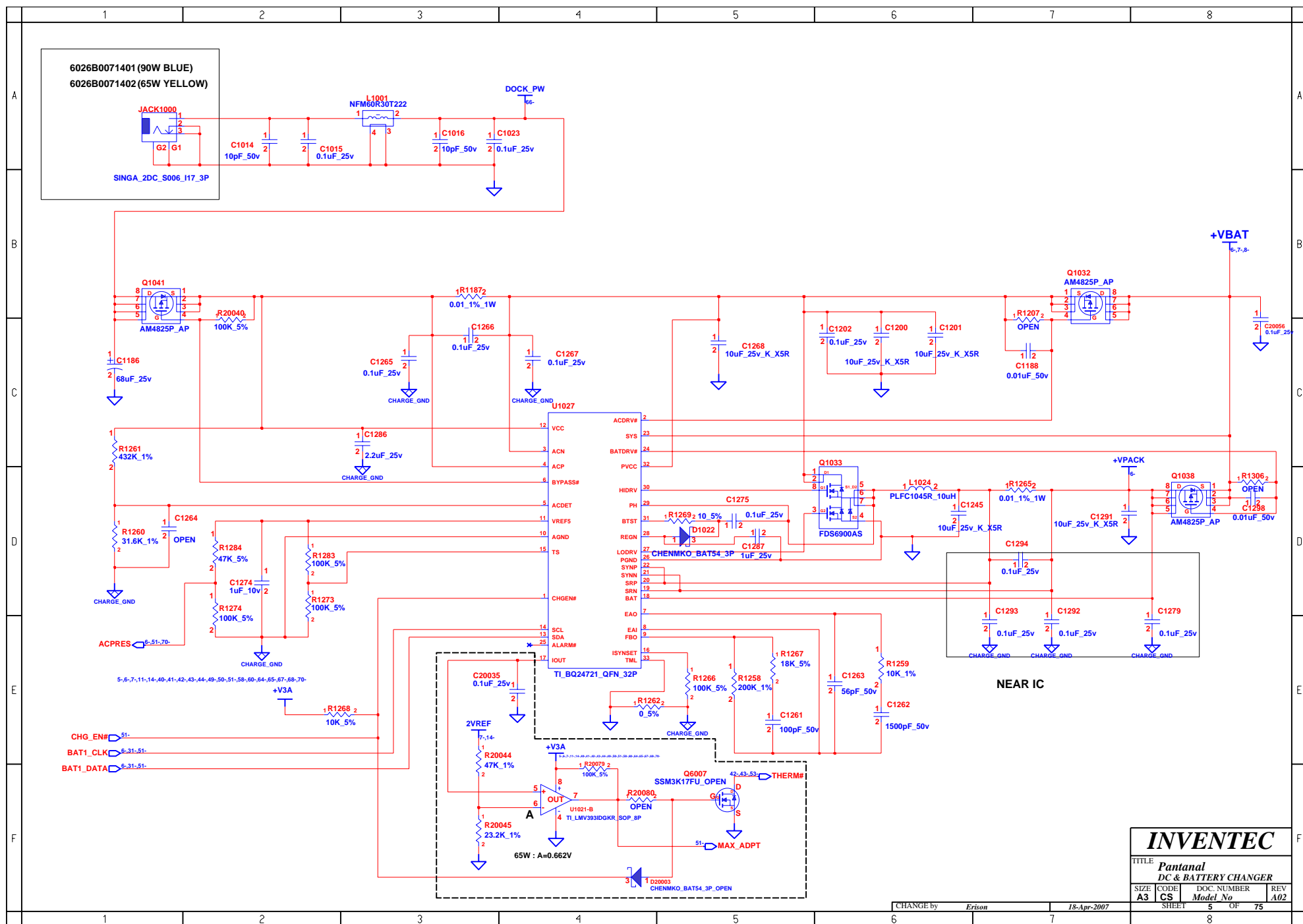




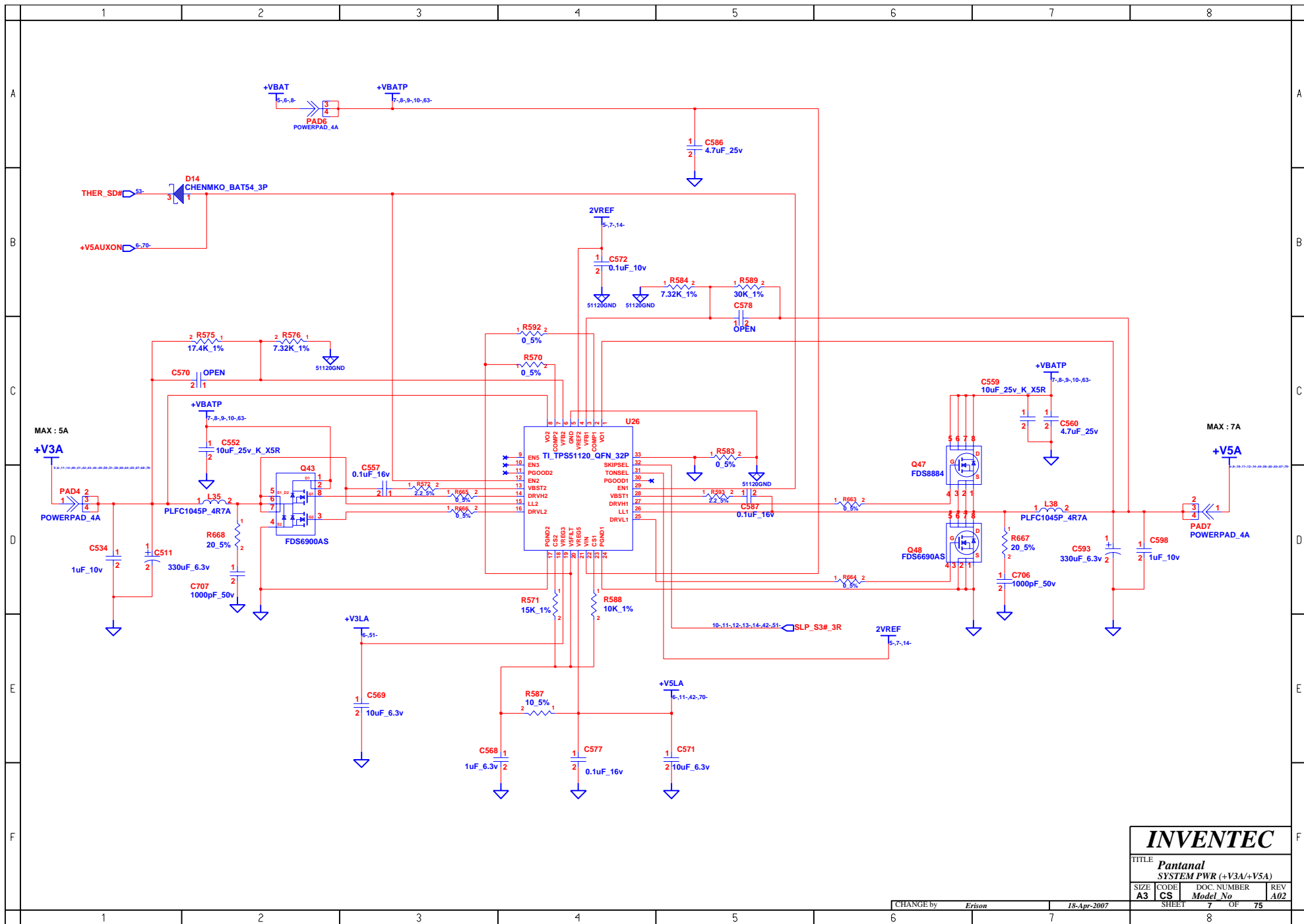
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BLOCK DIAGRAM			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER	REV <b>A02</b>
Model No		SHEET <b>4</b> OF <b>75</b>	

CHANGE by *Erison* 18-Apr-2007







# INVENTEC

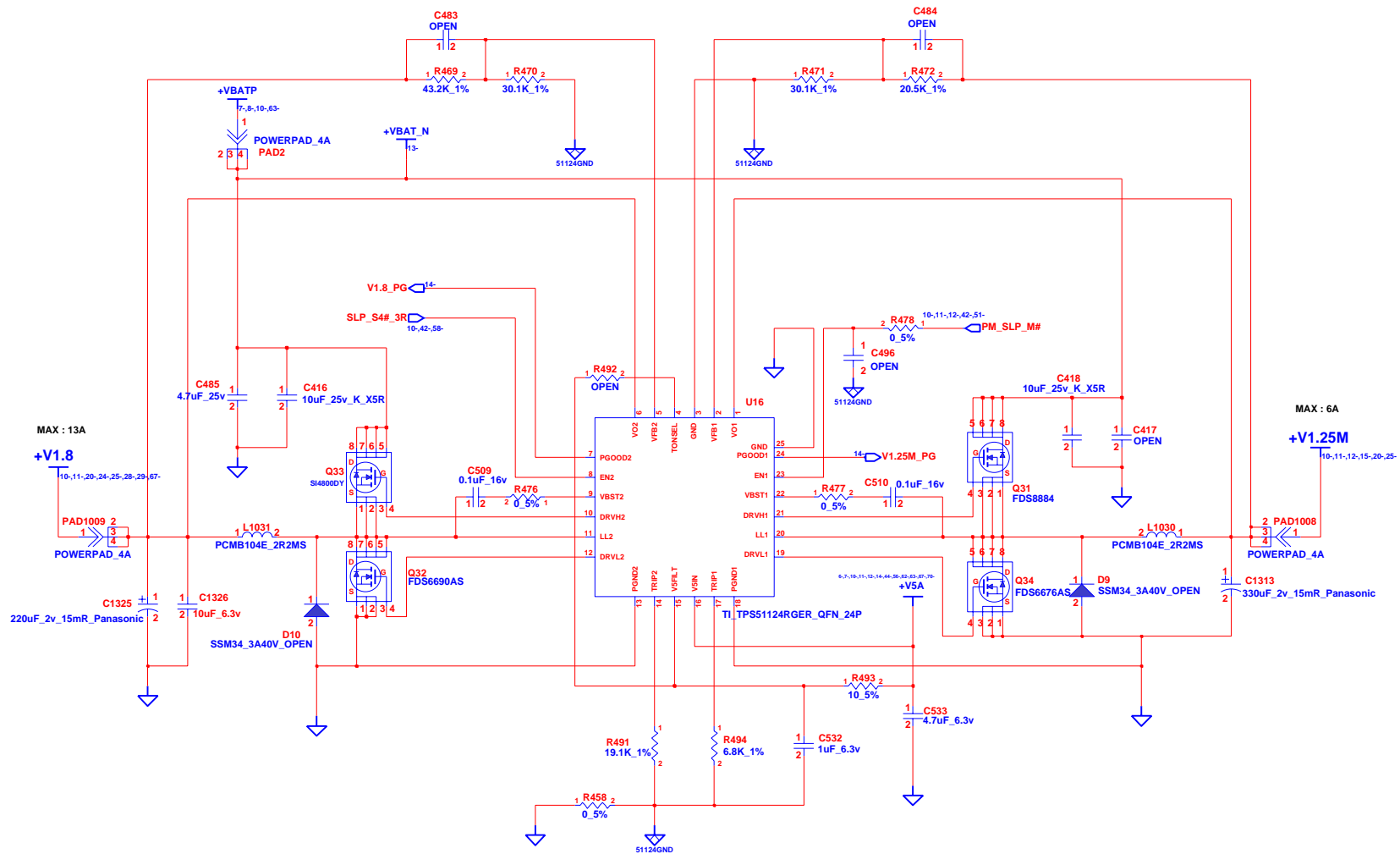
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Pantanal			
SYSTEM PWR (+V3A/+V5A)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by Erison 18-Apr-2007

SHEET 7 OF 75







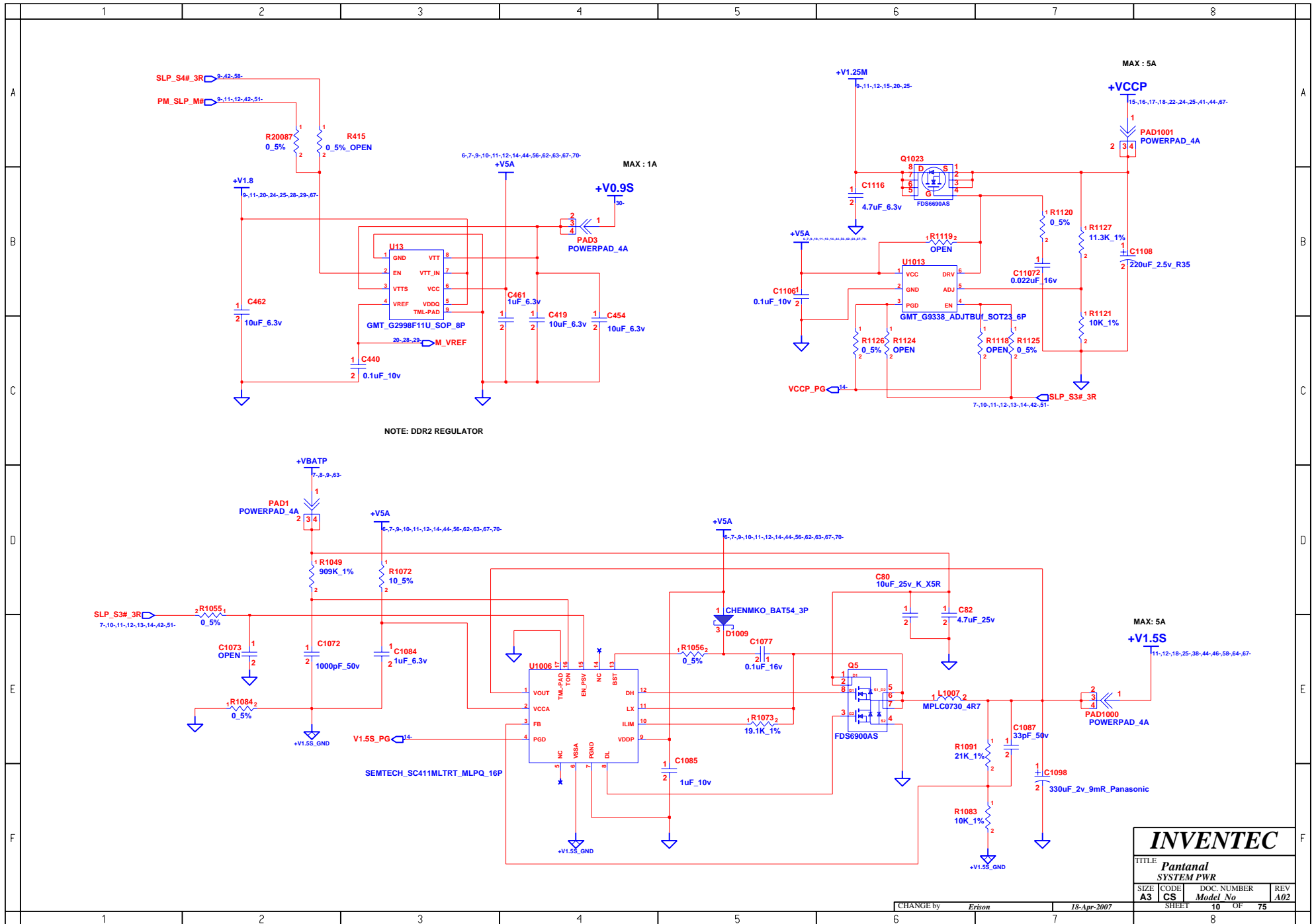
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**Pantanal  
SYSTEM PWR**

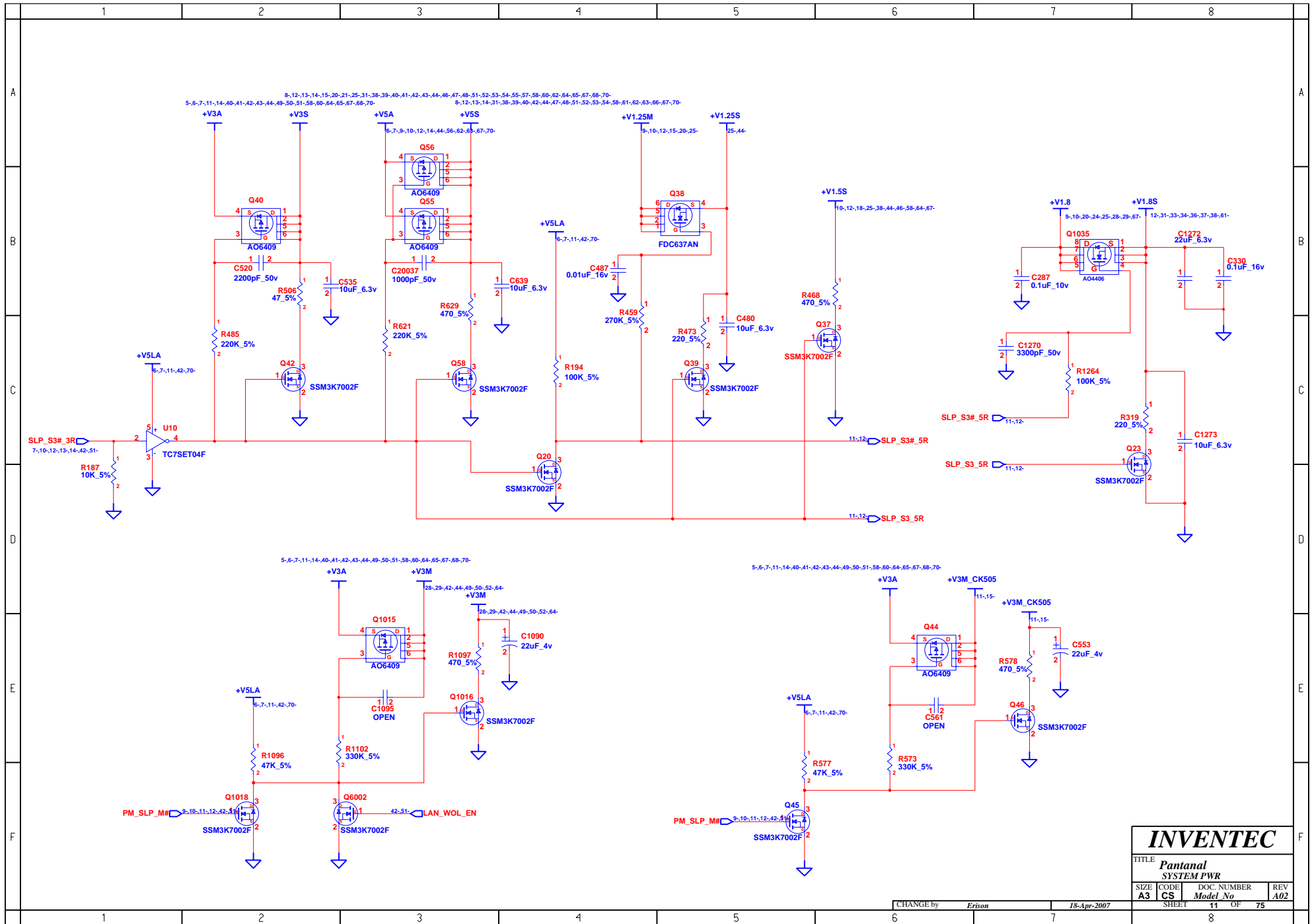
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A3	CS	Model No	A02

CHANGE by *Erison* 18-Apr-2007

SHEET 9 OF 75

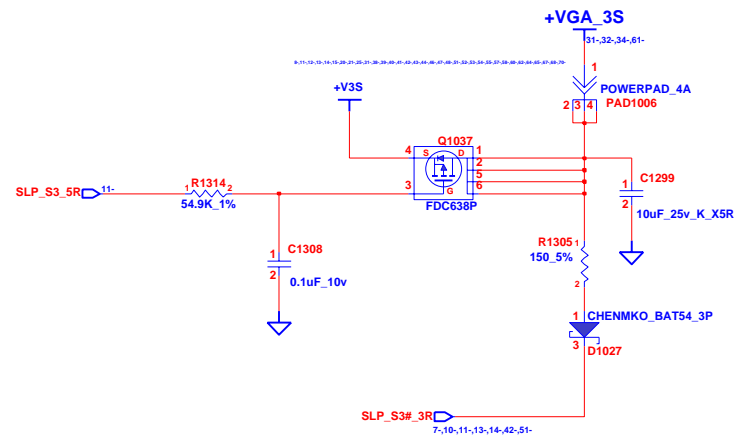
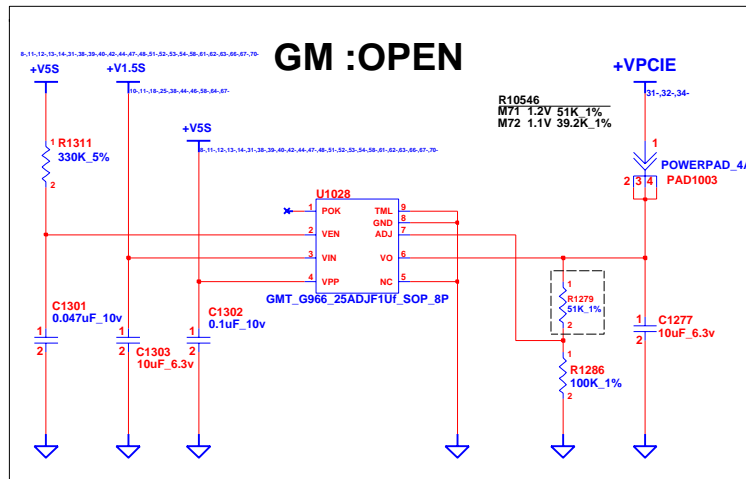
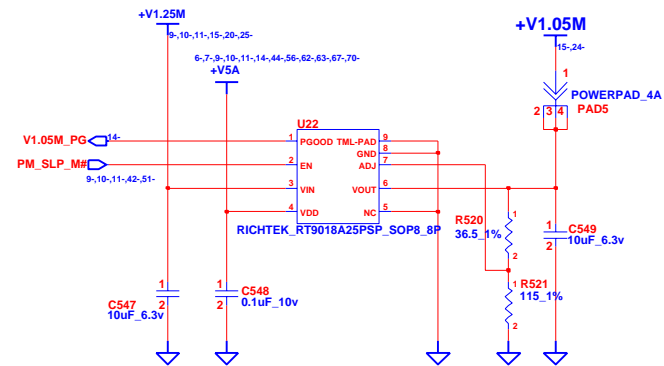
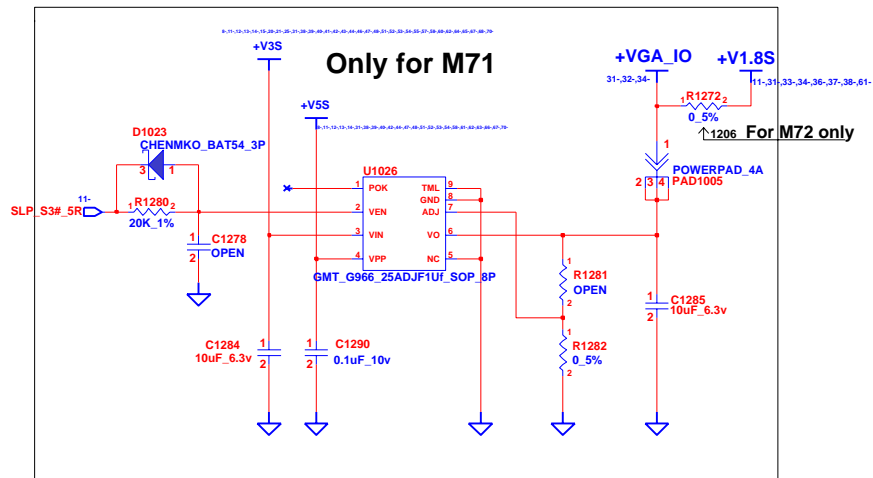


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Pantanal			
SYSTEM PWR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02



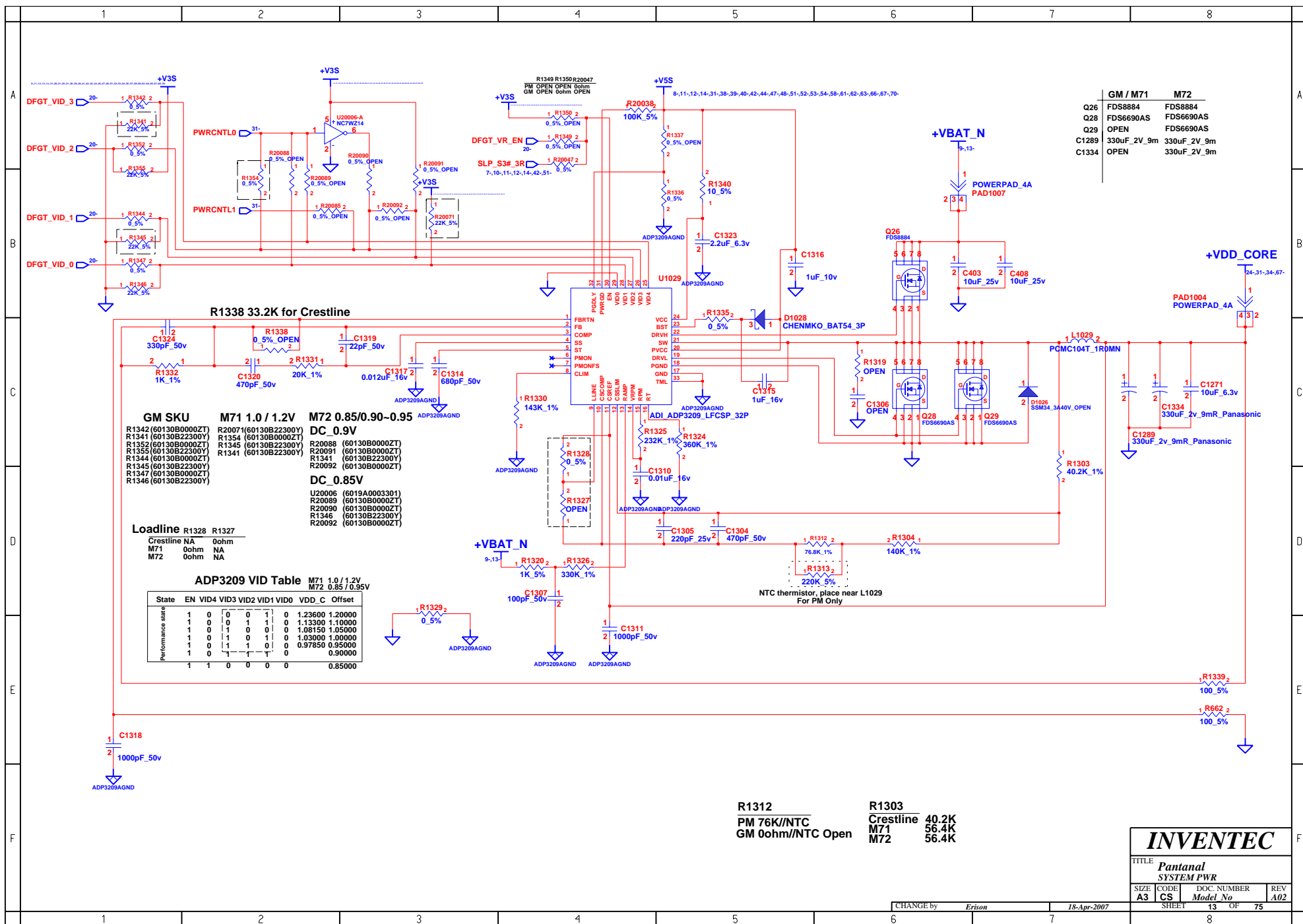
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TITLE <b>Pantanal SYSTEM PWR</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>Model No</b>	REV <b>A02</b>
CHANGE by <i>Erison</i>		18-Apr-2007	
SHEET <b>11</b>		OF <b>75</b>	

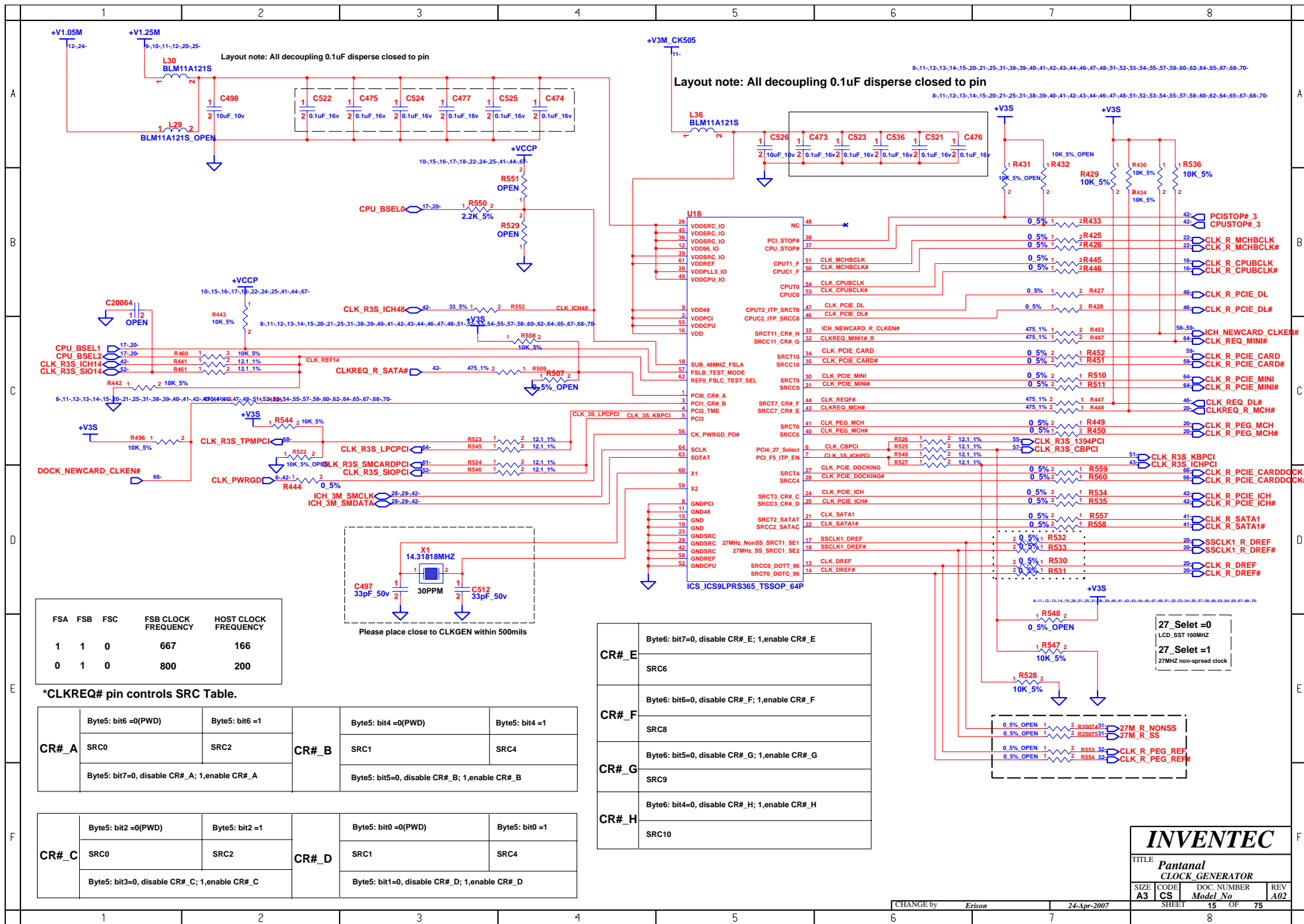


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TITLE <i>Pantanal SYSTEM PWR</i>				
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <i>Model No</i>		REV <i>A02</i>
SHEET		12	OF	75

CHANGE by: Erison 18-Apr-2007







FSA	FSB	FSC	FSB CLOCK FREQUENCY	HOST CLOCK FREQUENCY
1	1	0	667	166
0	1	0	800	200

\*CLKREQ# pin controls SRC Table.

CR#_A	Byte5: bit6 =0(PWD)	Byte5: bit6 =1	CR#_B	Byte5: bit4 =0(PWD)	Byte5: bit4 =1
	SRC0	SRC2		SRC1	SRC4
	Byte5: bit7=0, disable CR#_A; 1,enable CR#_A			Byte5: bit5=0, disable CR#_B; 1,enable CR#_B	

CR#_C	Byte5: bit2 =0(PWD)	Byte5: bit2 =1	CR#_D	Byte5: bit0 =0(PWD)	Byte5: bit0 =1
	SRC0	SRC2		SRC1	SRC4
	Byte5: bit3=0, disable CR#_C; 1,enable CR#_C			Byte5: bit1=0, disable CR#_D; 1,enable CR#_D	

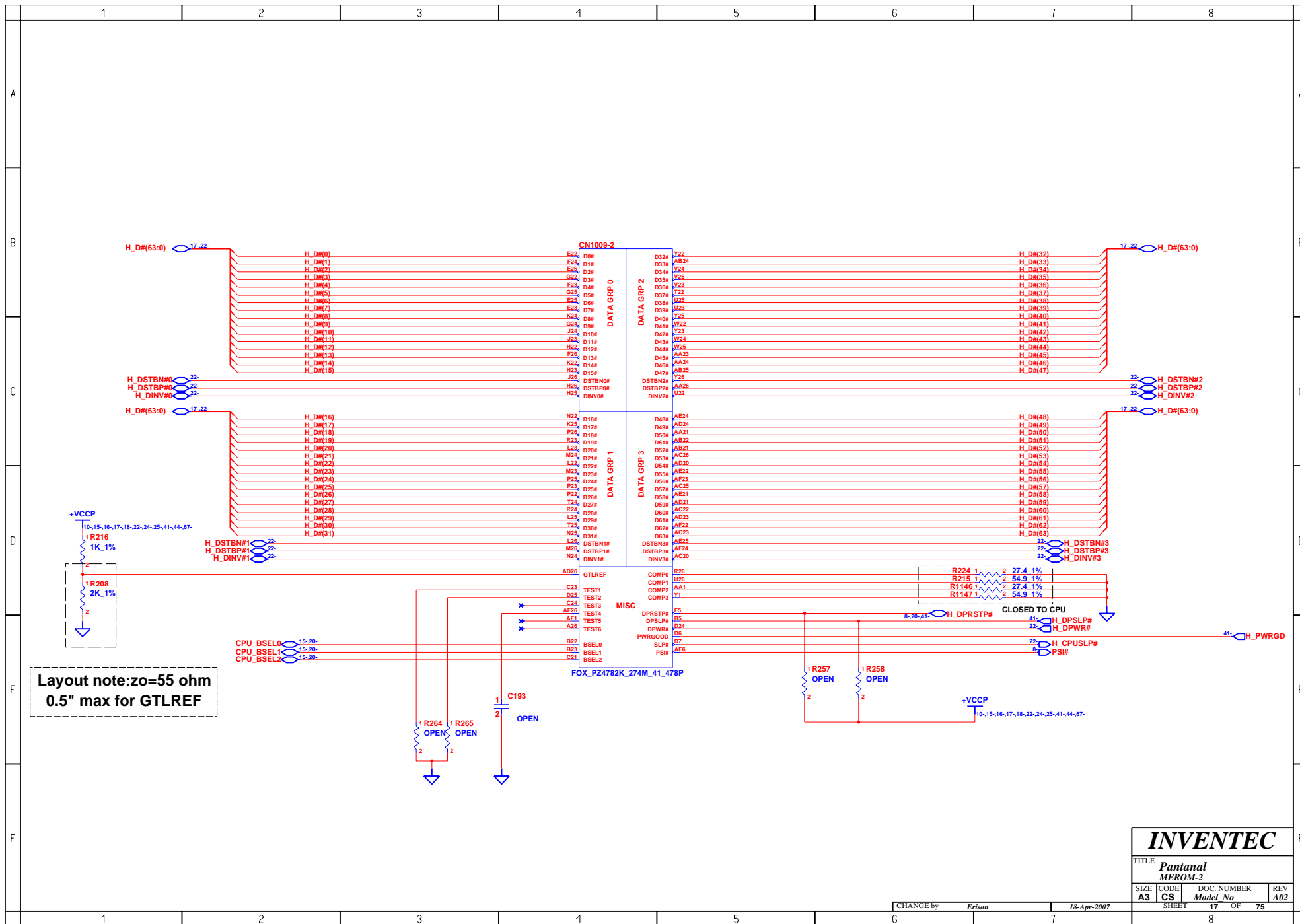
CR#_E	Byte6: bit7=0, disable CR#_E; 1,enable CR#_E
SRC6	
CR#_F	Byte6: bit6=0, disable CR#_F; 1,enable CR#_F
SRC8	
CR#_G	Byte6: bit5=0, disable CR#_G; 1,enable CR#_G
SRC9	
CR#_H	Byte6: bit4=0, disable CR#_H; 1,enable CR#_H
SRC10	

INVENTEC

TITLE			
<i>Pantanal</i> <i>CLOCK_GENERATOR</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02





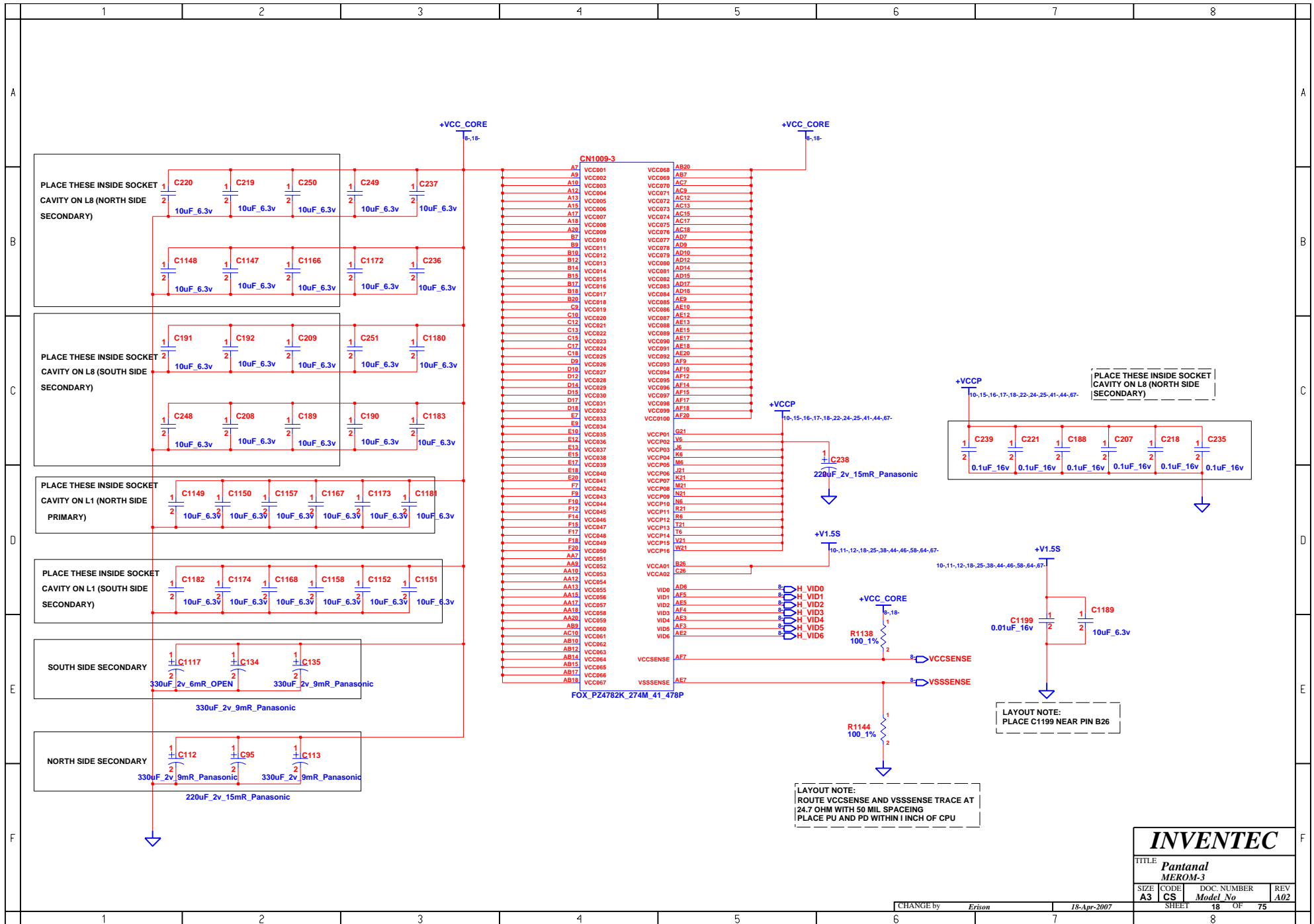


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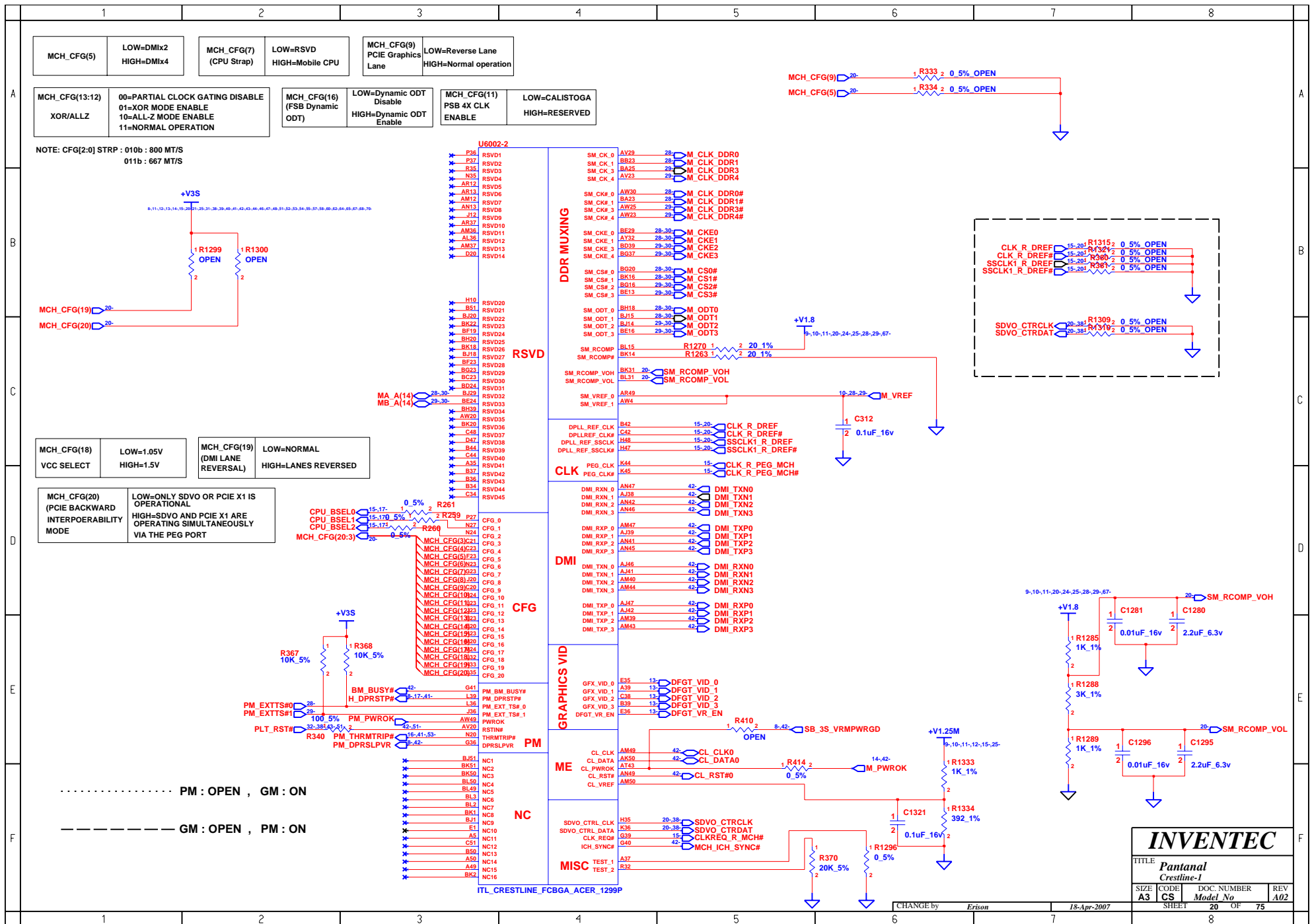
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Pantanal			
MEROM-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

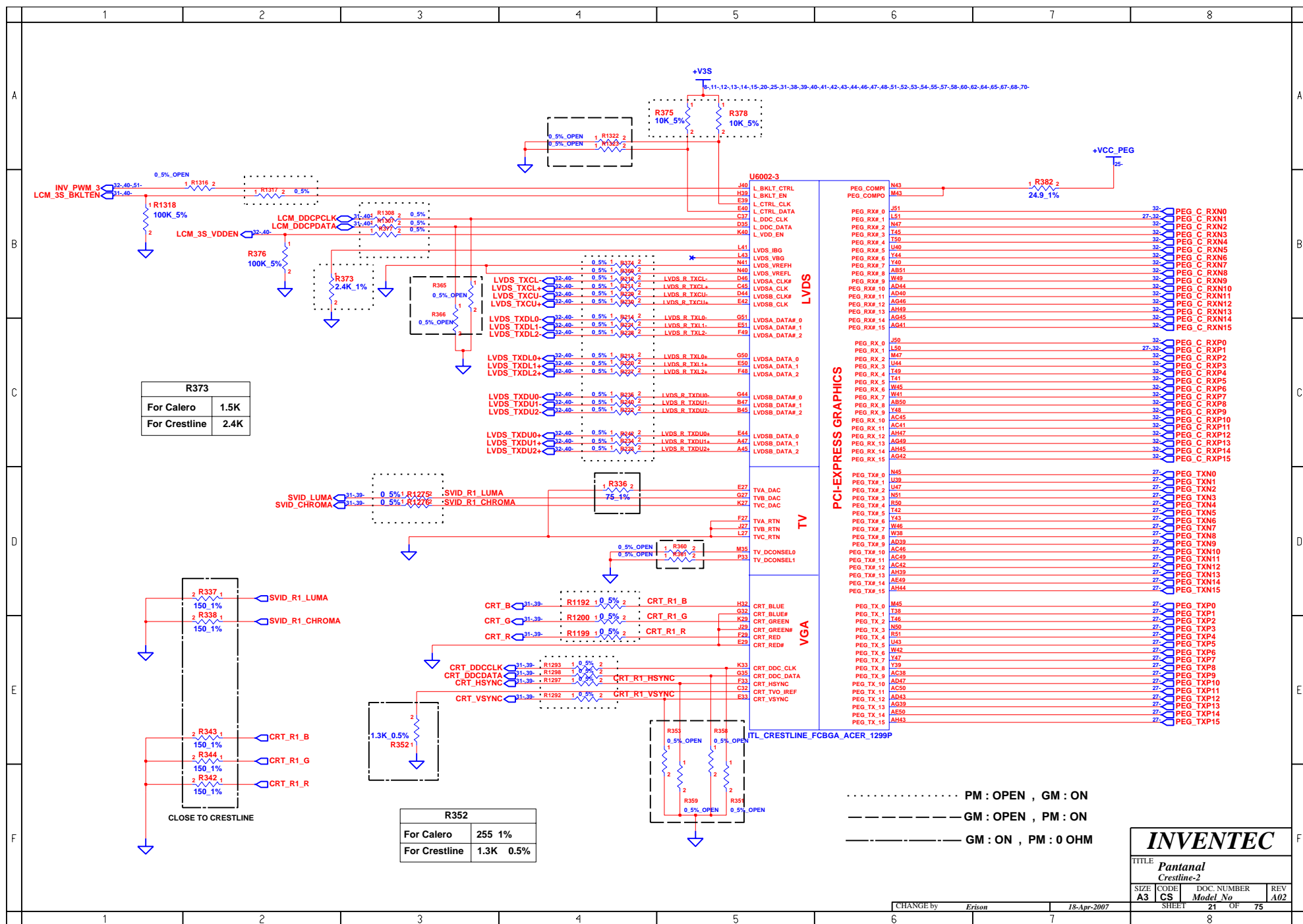
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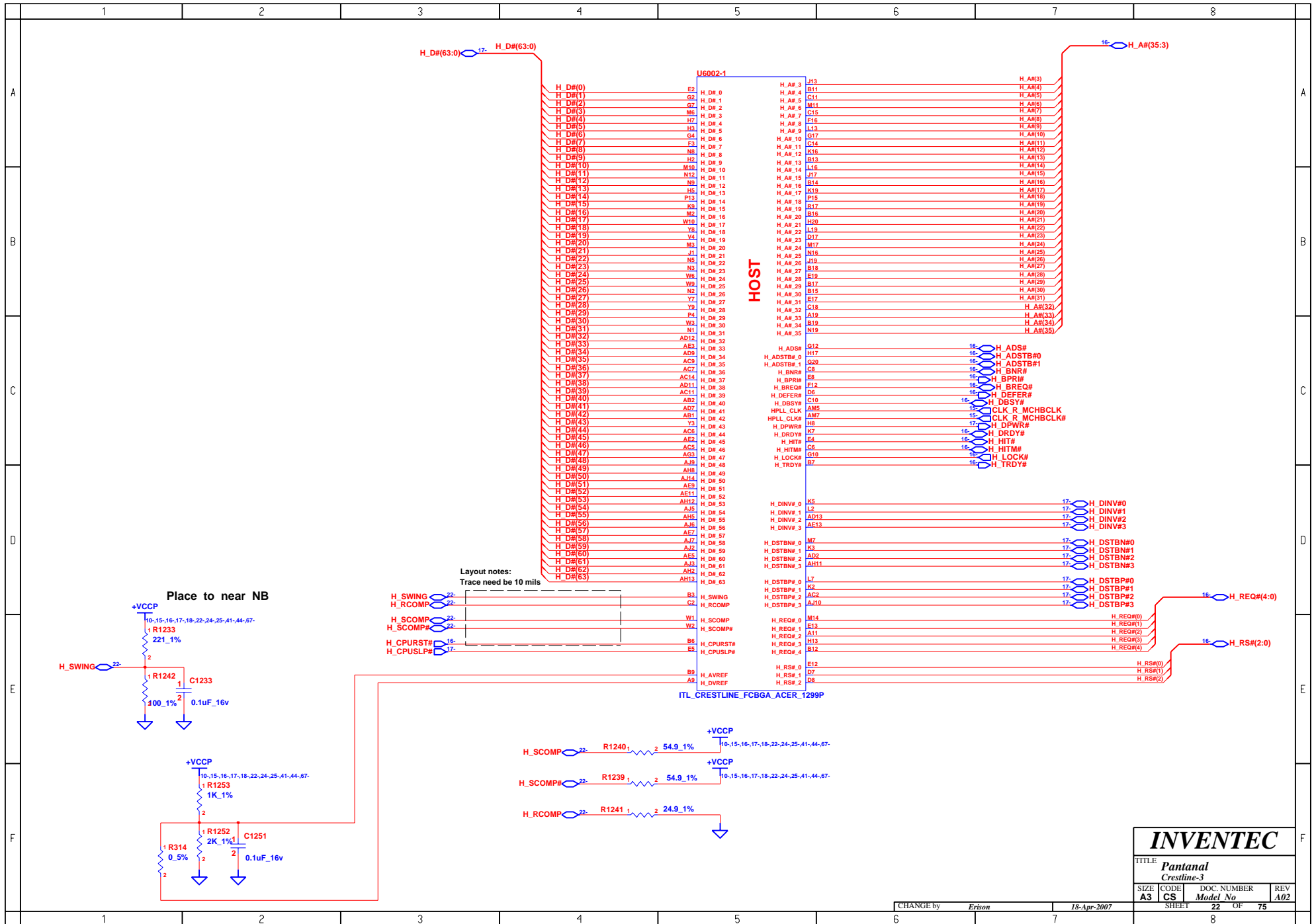
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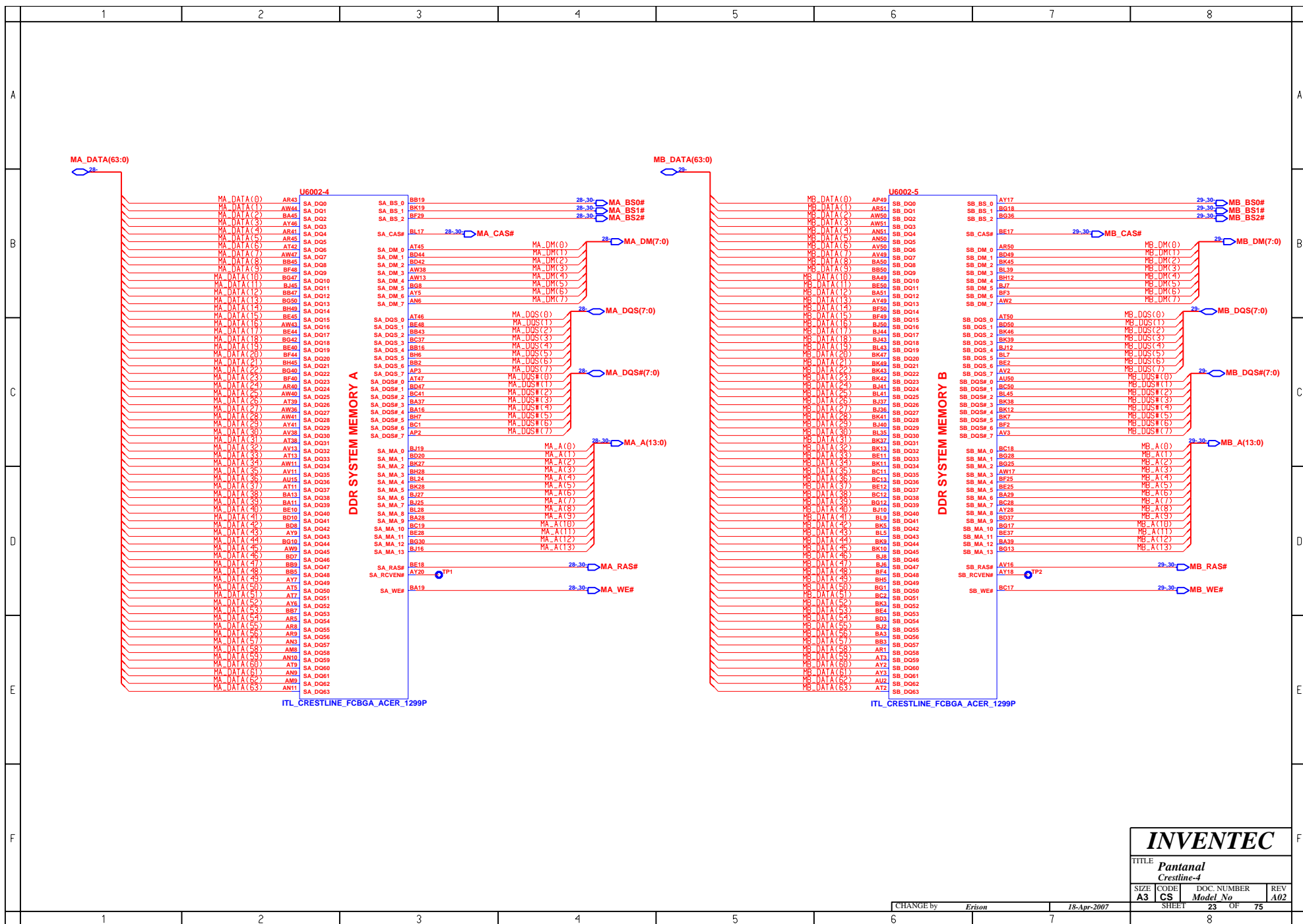


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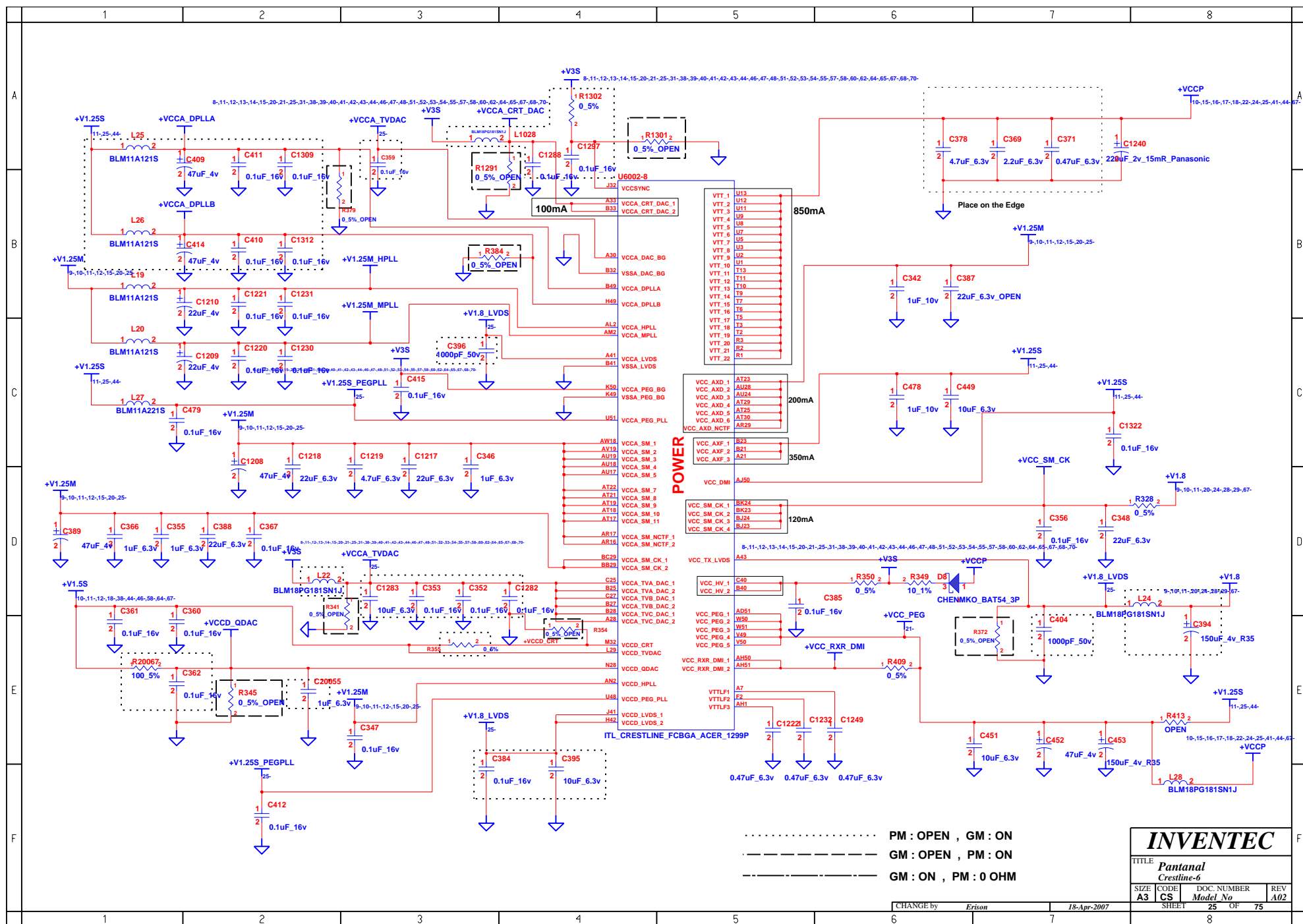


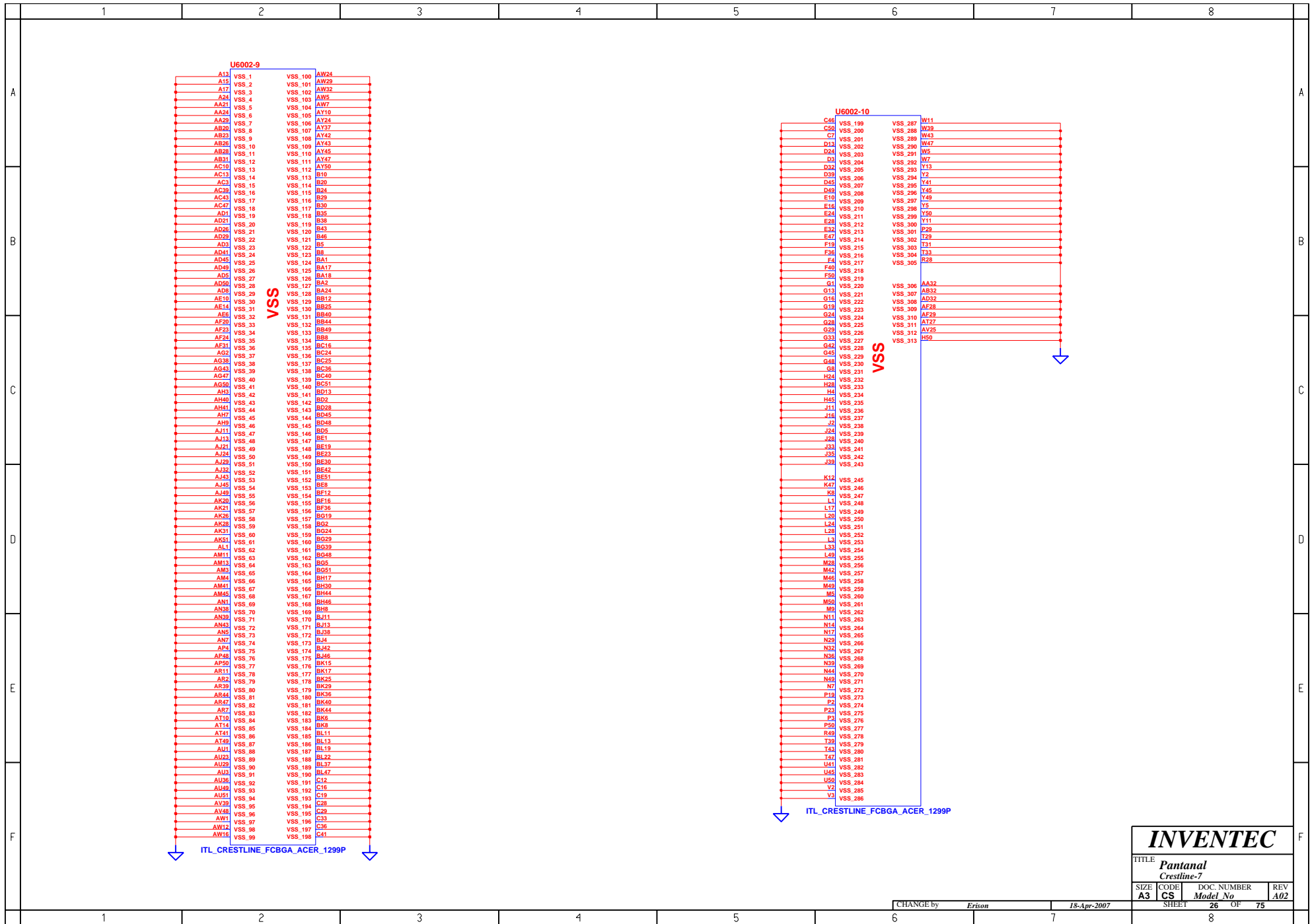












**INVENTEC**

TITLE			
Pantanal			
Crestline-7			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by *Erison* 18-Apr-2007

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	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

PEG\_TXP0

21-27-

C441

1

2

0.1uF\_16v

32-

PEG\_C\_TXP0

PEG\_TXP1

21-27-

C447

1

2

0.1uF\_16v

32-

PEG\_C\_TXP1

PEG\_TXP2

21-27-

C445

1

2

0.1uF\_16v

32-

PEG\_C\_TXP2

PEG\_TXP3

21-27-

C443

1

2

0.1uF\_16v

32-

PEG\_C\_TXP3

PEG\_TXP4

21-

C458

1

2

0.1uF\_16v

32-

PEG\_C\_TXP4

PEG\_TXP5

21-

C456

1

2

0.1uF\_16v

32-

PEG\_C\_TXP5

PEG\_TXP6

21-

C432

1

2

0.1uF\_16v

32-

PEG\_C\_TXP6

PEG\_TXP7

21-

C466

1

2

0.1uF\_16v

32-

PEG\_C\_TXP7

PEG\_TXP8

21-

C431

1

2

0.1uF\_16v

32-

PEG\_C\_TXP8

PEG\_TXP9

21-

C434

1

2

0.1uF\_16v

32-

PEG\_C\_TXP9

PEG\_TXP10

21-

C470

1

2

0.1uF\_16v

32-

PEG\_C\_TXP10

PEG\_TXP11

21-

C468

1

2

0.1uF\_16v

32-

PEG\_C\_TXP11

PEG\_TXP12

21-

C437

1

2

0.1uF\_16v

32-

PEG\_C\_TXP12

PEG\_TXP13

21-

C438

1

2

0.1uF\_16v

32-

PEG\_C\_TXP13

PEG\_TXP14

21-

C472

1

2

0.1uF\_16v

32-

PEG\_C\_TXP14

PEG\_TXP15

21-

C459

1

2

0.1uF\_16v

32-

PEG\_C\_TXP15

PEG\_TXN0

21-27-

C442

1

2

0.1uF\_16v

32-

PEG\_C\_TXN0

PEG\_TXN1

21-27-

C448

1

2

0.1uF\_16v

32-

PEG\_C\_TXN1

PEG\_TXN2

21-27-

C446

1

2

0.1uF\_16v

32-

PEG\_C\_TXN2

PEG\_TXN3

21-27-

C444

1

2

0.1uF\_16v

32-

PEG\_C\_TXN3

PEG\_TXN4

21-

C457

1

2

0.1uF\_16v

32-

PEG\_C\_TXN4

PEG\_TXN5

21-

C455

1

2

0.1uF\_16v

32-

PEG\_C\_TXN5

PEG\_TXN6

21-

C433

1

2

0.1uF\_16v

32-

PEG\_C\_TXN6

PEG\_TXN7

21-

C465

1

2

0.1uF\_16v

32-

PEG\_C\_TXN7

PEG\_TXN8

21-

C430

1

2

0.1uF\_16v

32-

PEG\_C\_TXN8

PEG\_TXN9

21-

C435

1

2

0.1uF\_16v

32-

PEG\_C\_TXN9

PEG\_TXN10

21-

C469

1

2

0.1uF\_16v

32-

PEG\_C\_TXN10

PEG\_TXN11

21-

C467

1

2

0.1uF\_16v

32-

PEG\_C\_TXN11

PEG\_TXN12

21-

C436

1

2

0.1uF\_16v

32-

PEG\_C\_TXN12

PEG\_TXN13

21-

C439

1

2

0.1uF\_16v

32-

PEG\_C\_TXN13

PEG\_TXN14

21-

C471

1

2

0.1uF\_16v

32-

PEG\_C\_TXN14

PEG\_TXN15

21-

C460

1

2

0.1uF\_16v

32-

PEG\_C\_TXN15

GM : OPEN

Place to near NB

PEG\_TXP0

21-27-

C424

1

2

OPEN

38-

SDVOB\_R+

PEG\_TXN0

21-27-

C425

1

2

OPEN

38-

SDVOB\_R-

PEG\_TXP1

21-27-

C422

1

2

OPEN

38-

SDVOB\_G+

PEG\_TXN1

21-27-

C423

1

2

OPEN

38-

SDVOB\_G-

PEG\_TXP2

21-27-

C428

1

2

OPEN

38-

SDVOB\_B+

PEG\_TXN2

21-27-

C429

1

2

OPEN

38-

SDVOB\_B-

PEG\_TXP3

21-27-

C426

1

2

OPEN

38-

SDVOB\_CLK+

PEG\_TXN3

21-27-

C427

1

2

OPEN

38-

SDVOB\_CLK-

PM : OPEN , GM : ORIGINAL

PEG\_C\_RXN1

21-32-

R394

1

2

0.5%\_OPEN

38-

PEG\_C1\_RXN1

PEG\_C\_RXP1

21-32-

R393

1

2

0.5%\_OPEN

38-

PEG\_C1\_RXP1

DVI : INSTALL

PM : OPEN , GM : SHORT

INVENTEC

TITLE

Pantanal

PCI-E to SDVO

SIZE

A3

CODE

CS

DOC. NUMBER

Model No

REV

A02

CHANGE by

Erison

18-Apr-2007

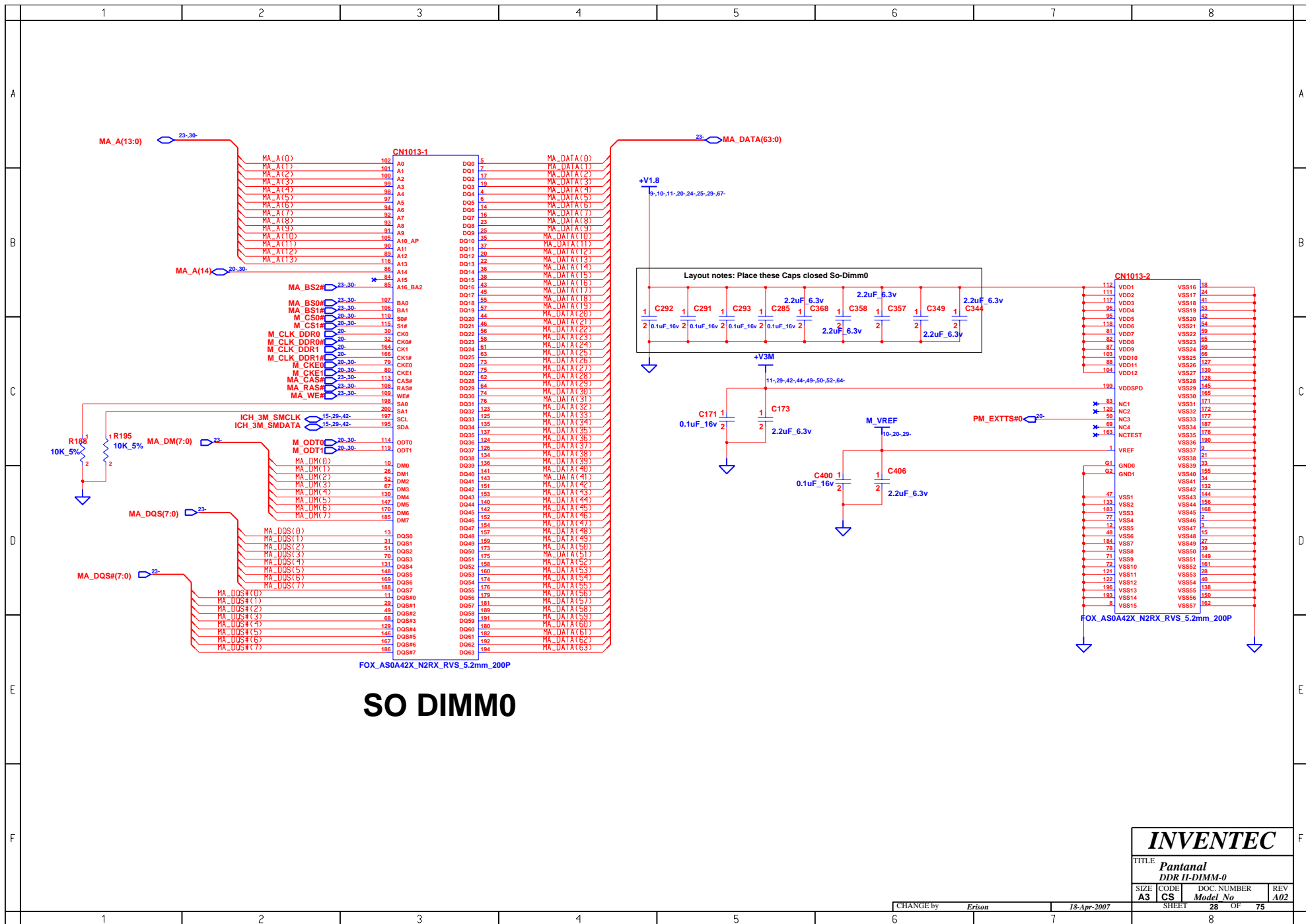
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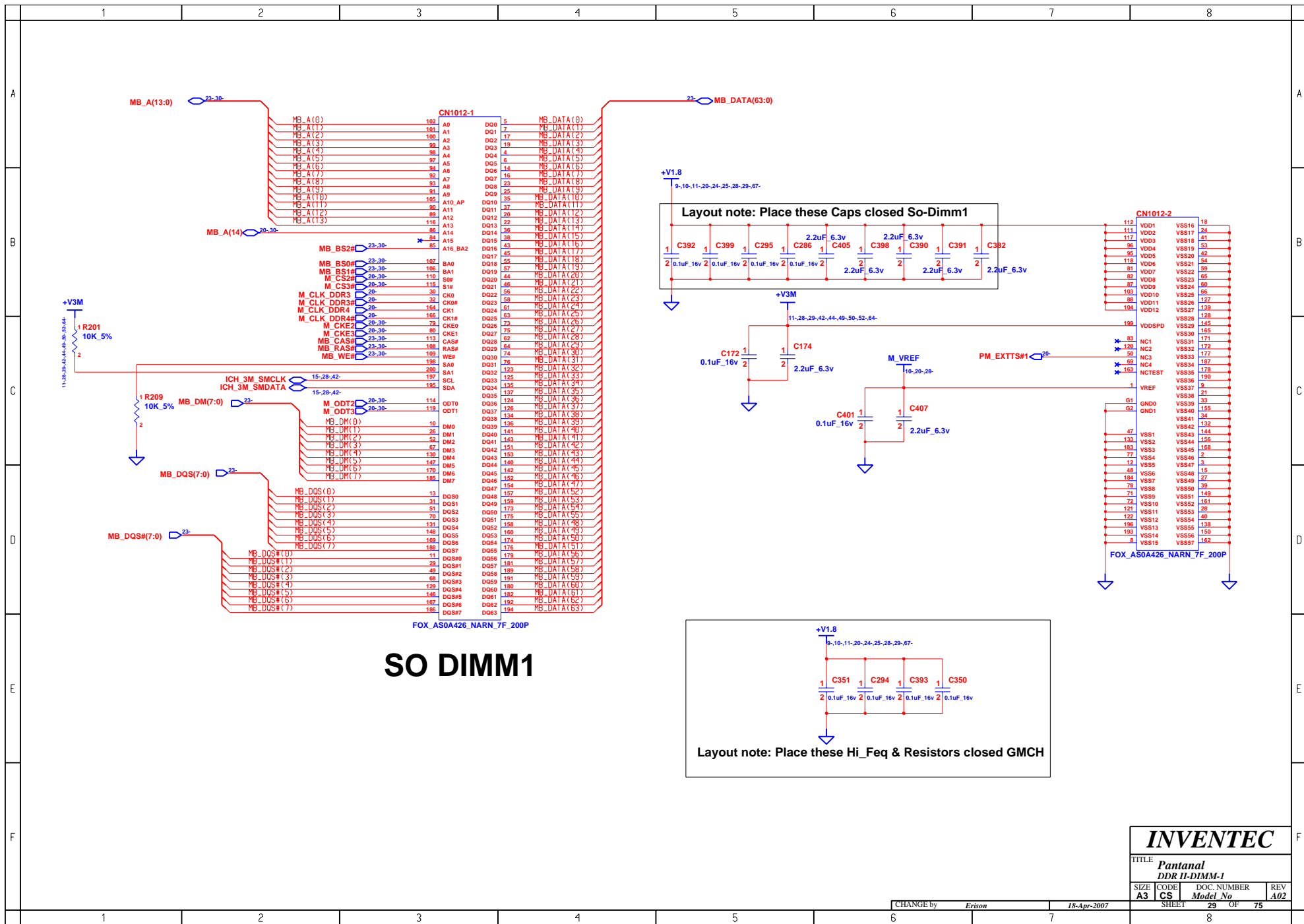
27

OF

75

8





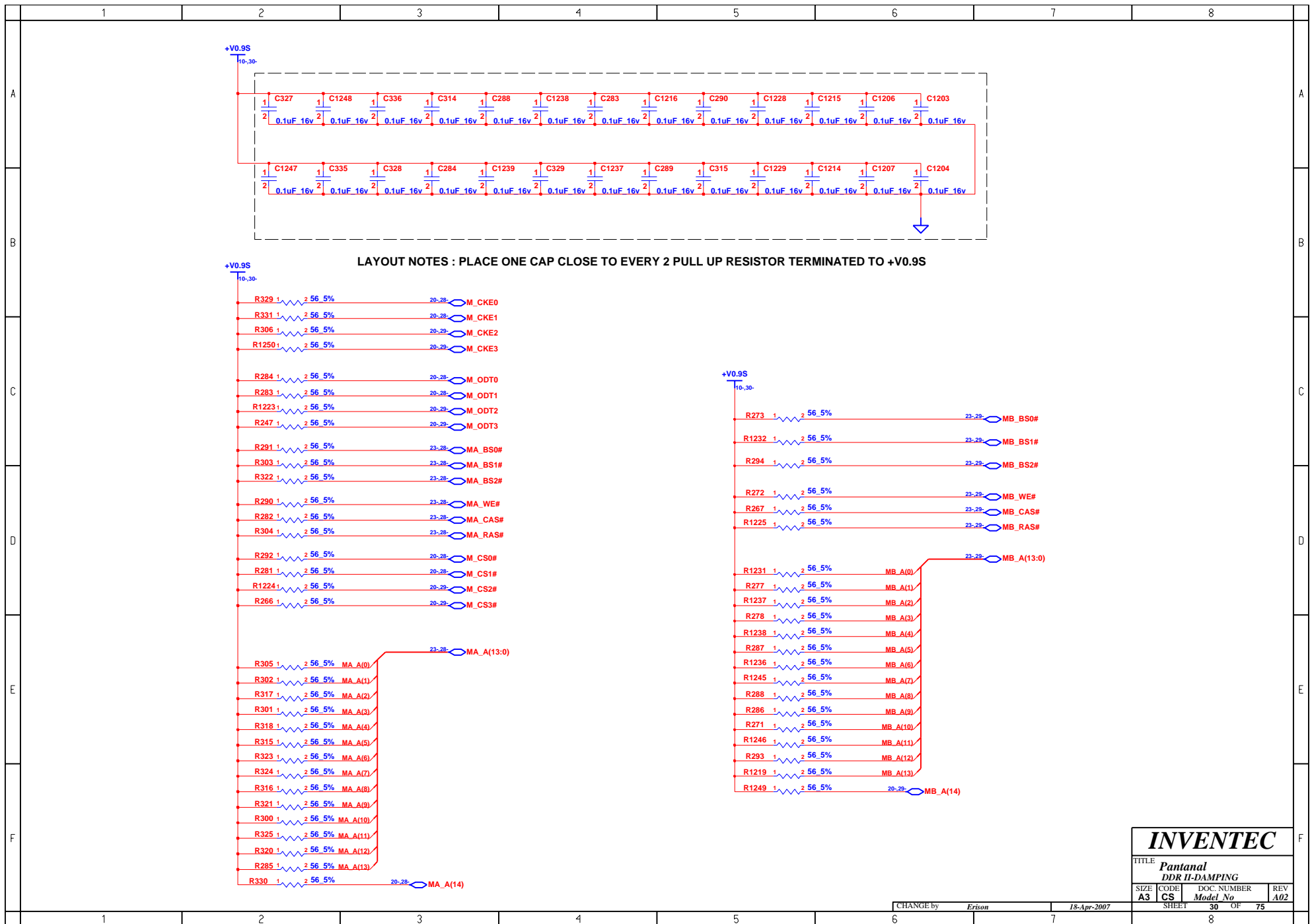
SO DIMM1

INVENTEC

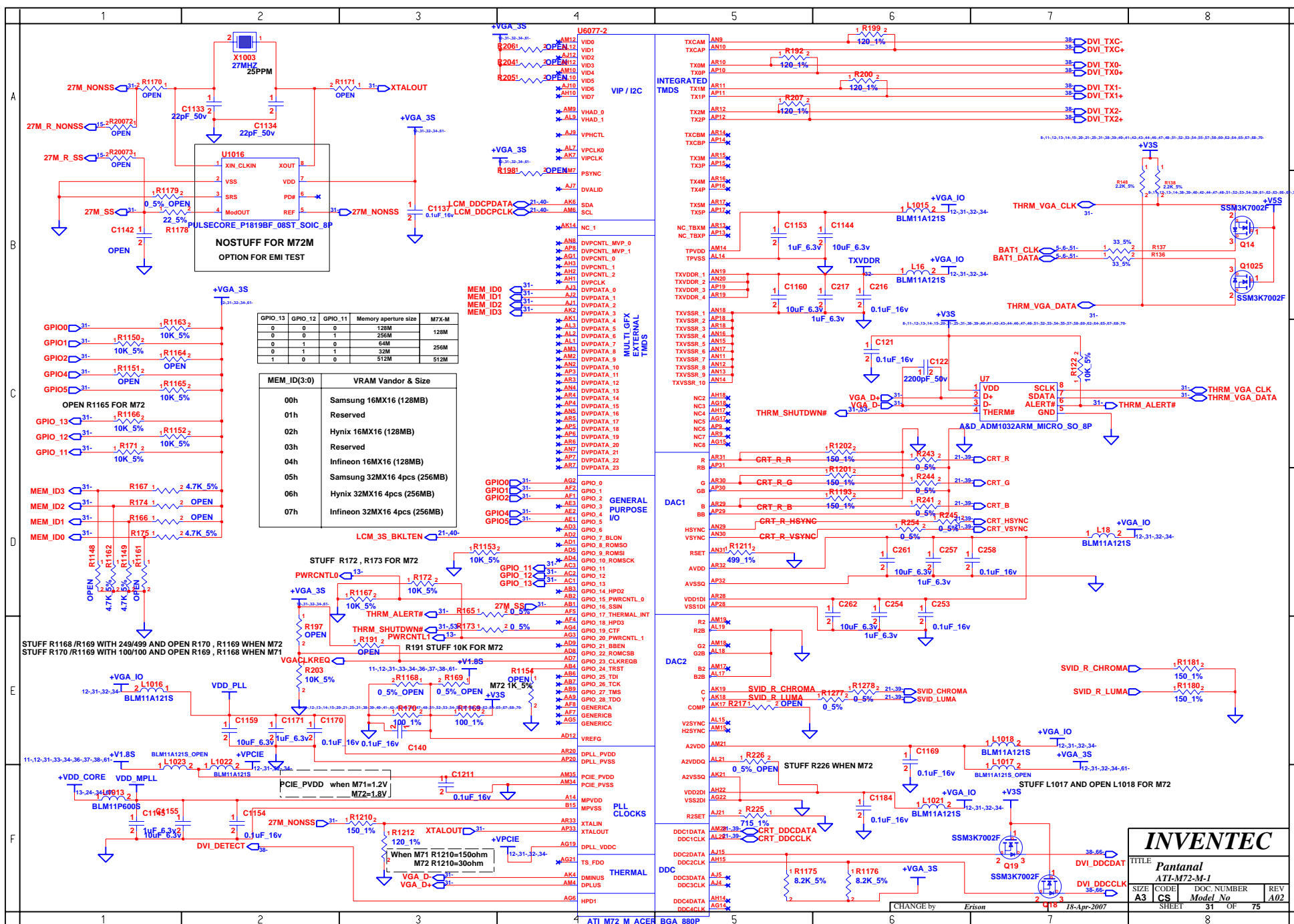
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Pantanal  
DDR II-DIMM-1  
SIZE  
CODE  
CS  
Model No  
REV  
A02

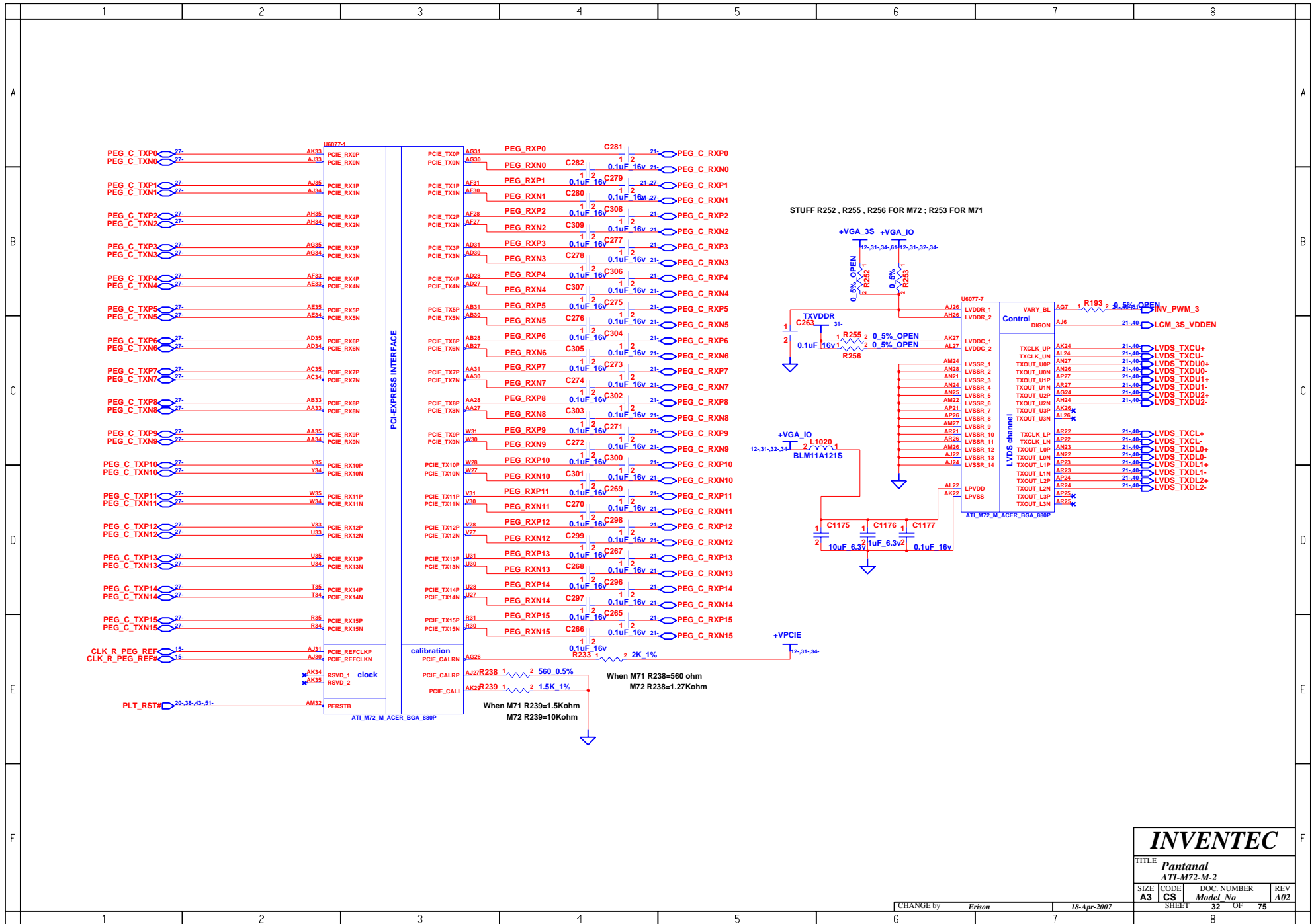
CHANGE by  
Erison  
18-Apr-2007

SHEET  
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INVENTEC			
TITLE Pantanal DDR II-DAMPING			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02
CHANGE by Erison		18-Apr-2007	
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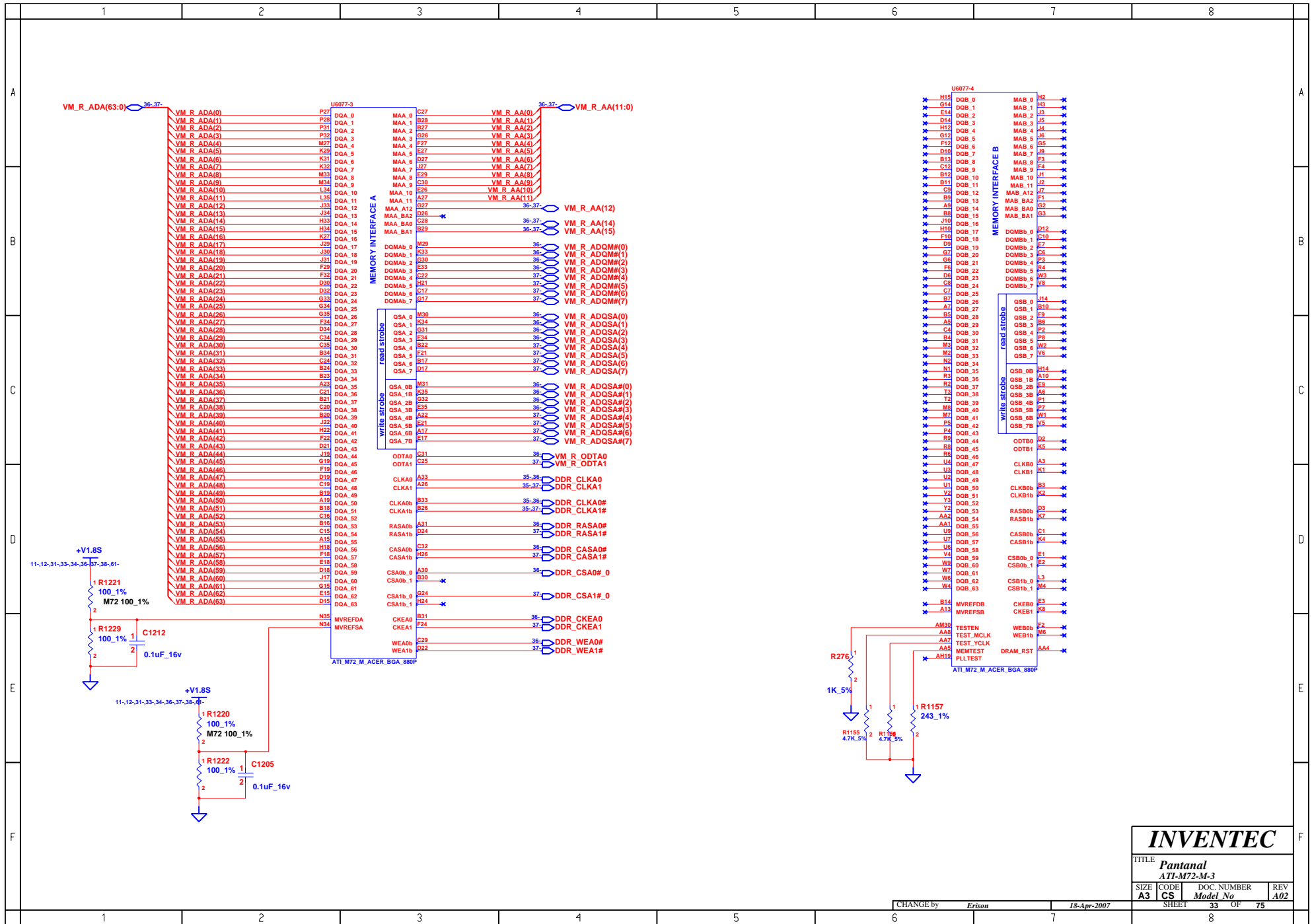
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TITLE  
Pantanal  
ATI-M72-M-2

SIZE CODE CS DOC. NUMBER REV A02

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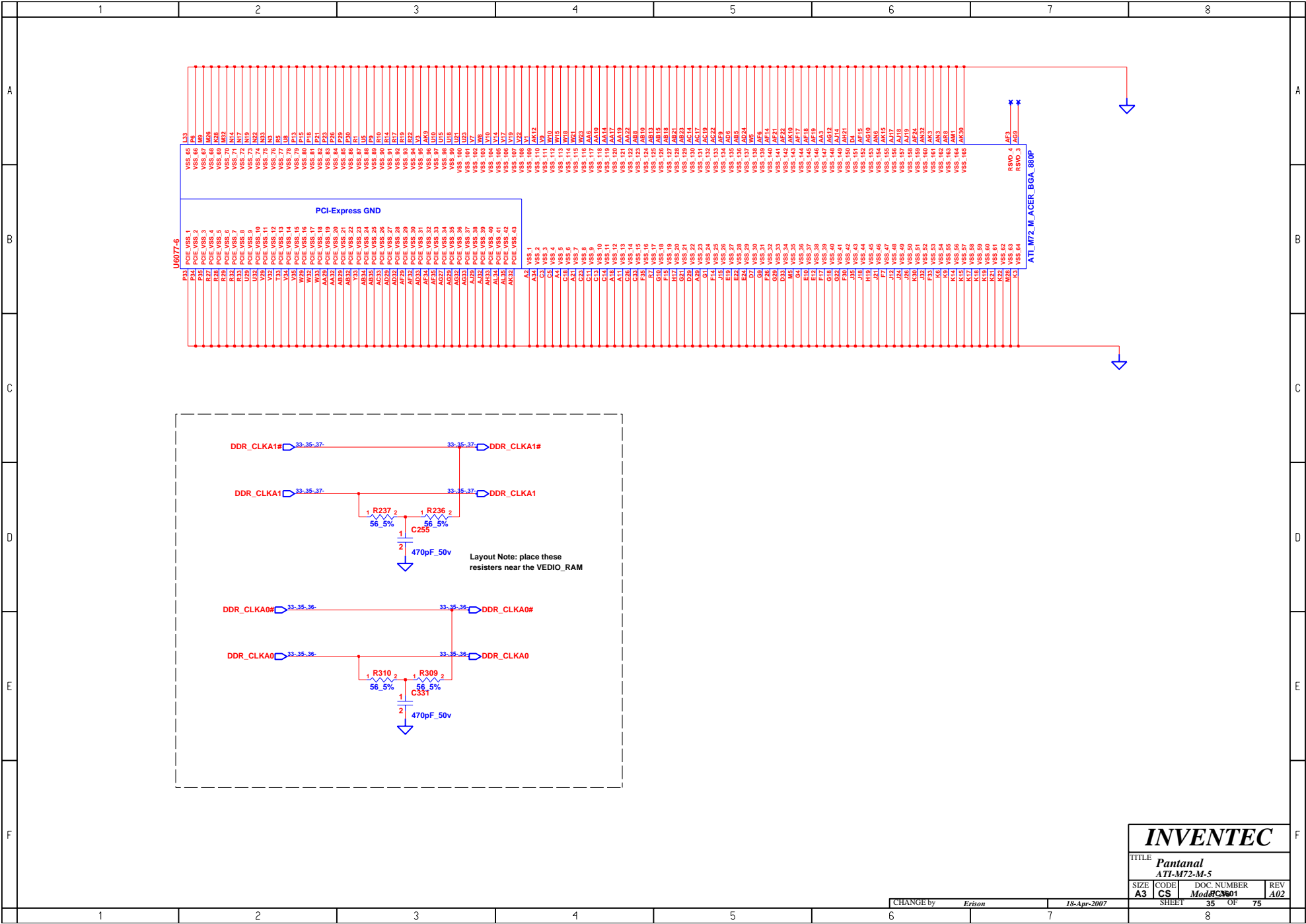
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ATI-M72-M-3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

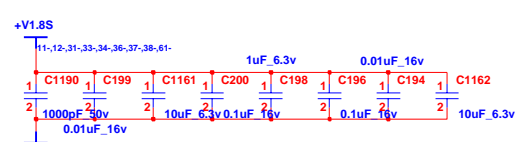
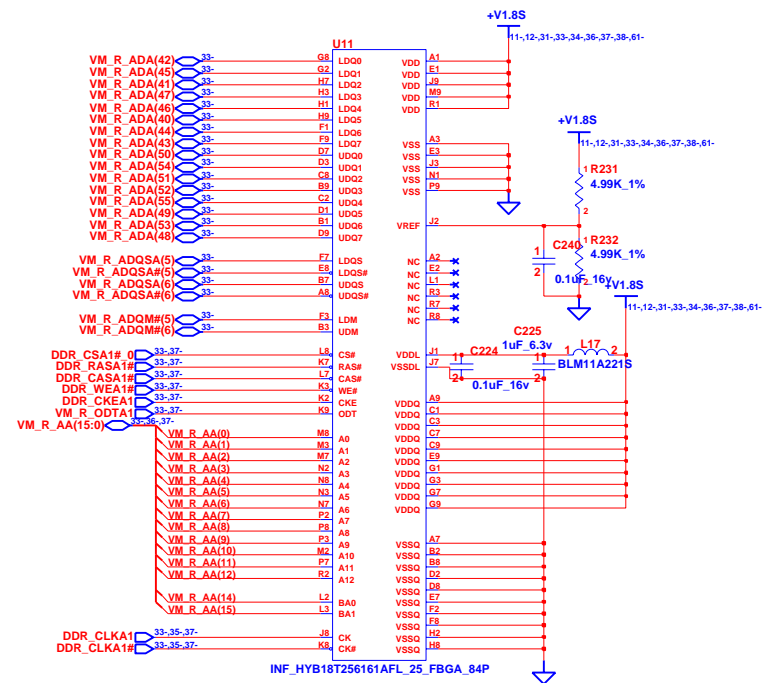
CHANGE by Erison 18-Apr-2007

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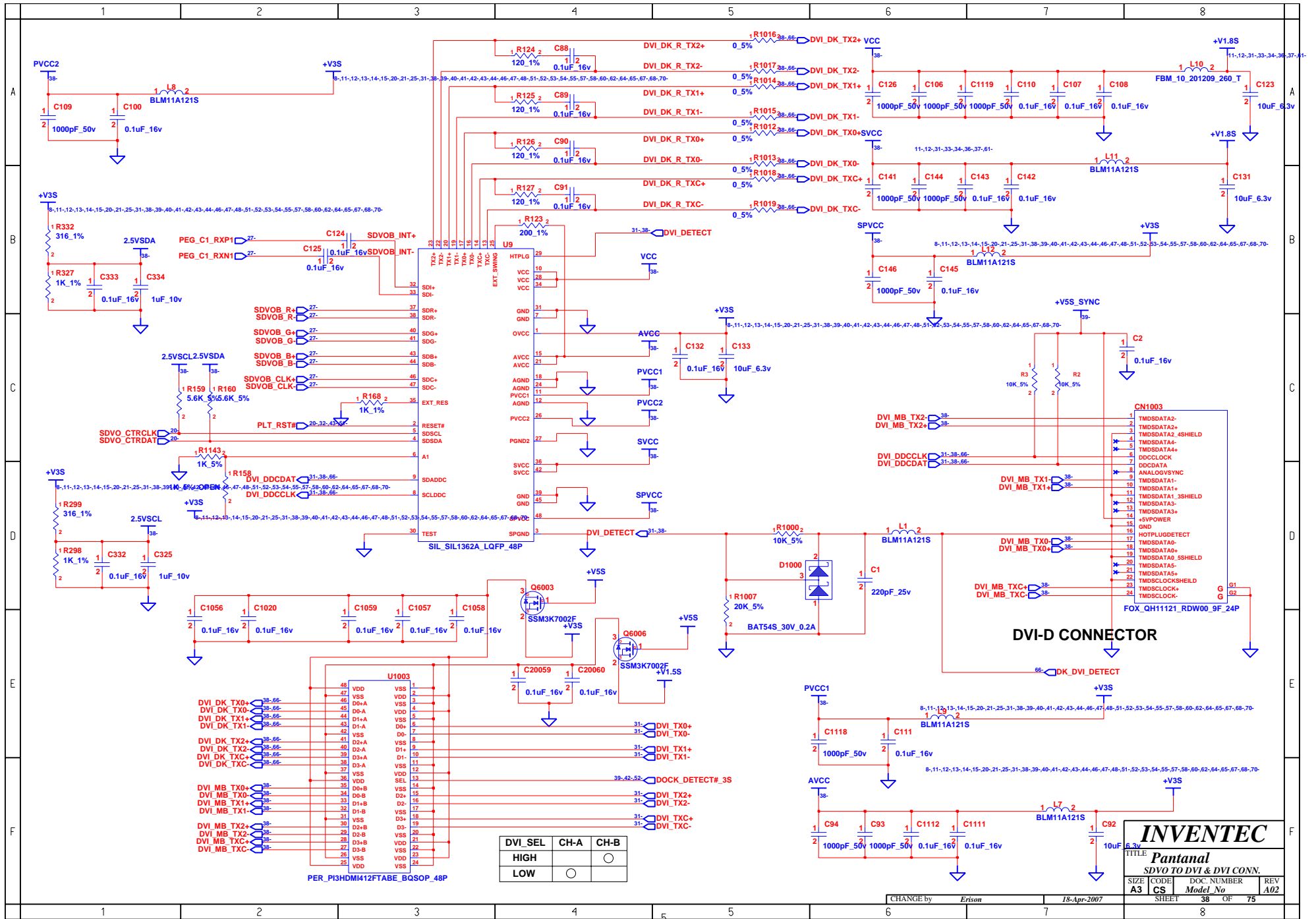


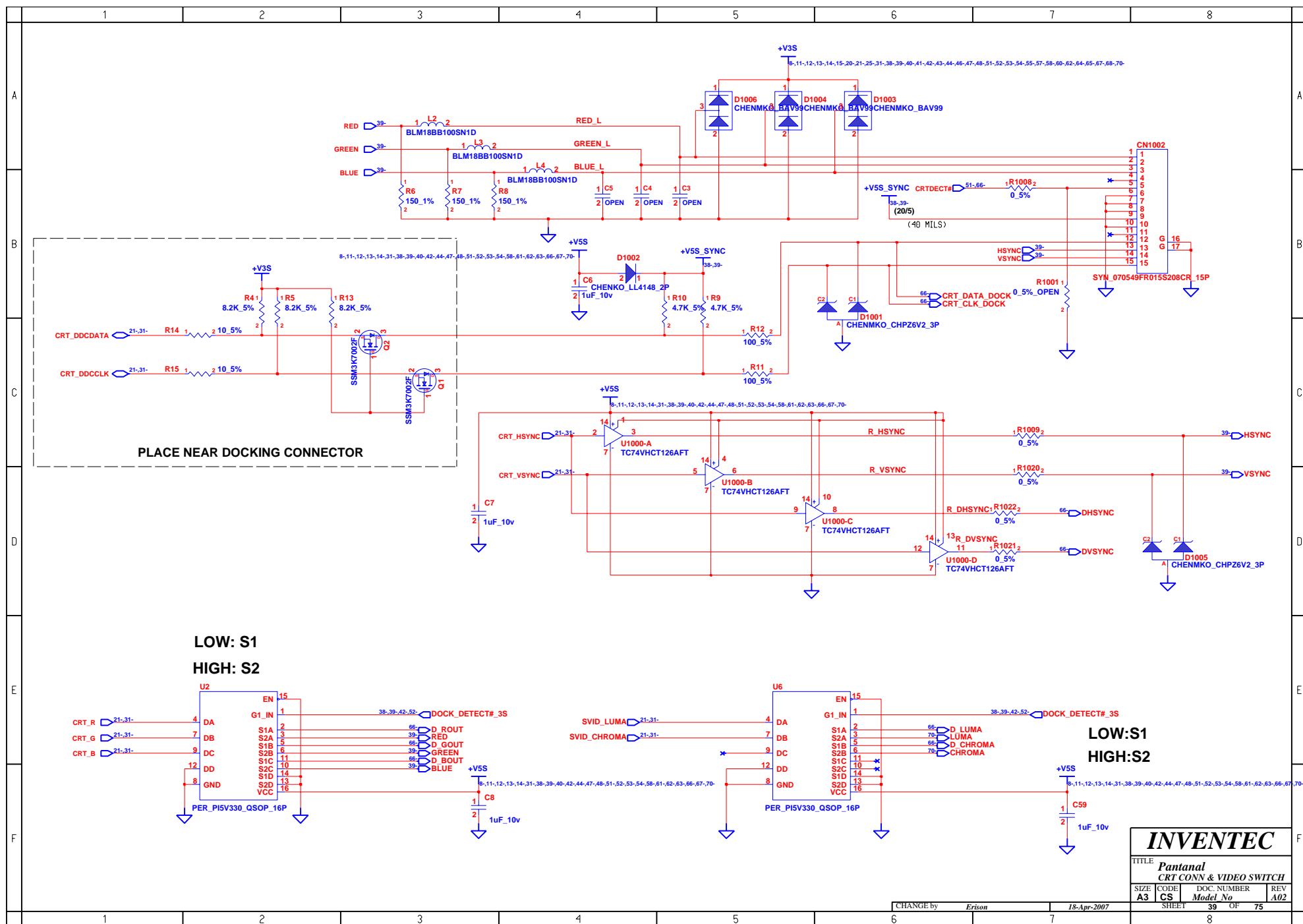


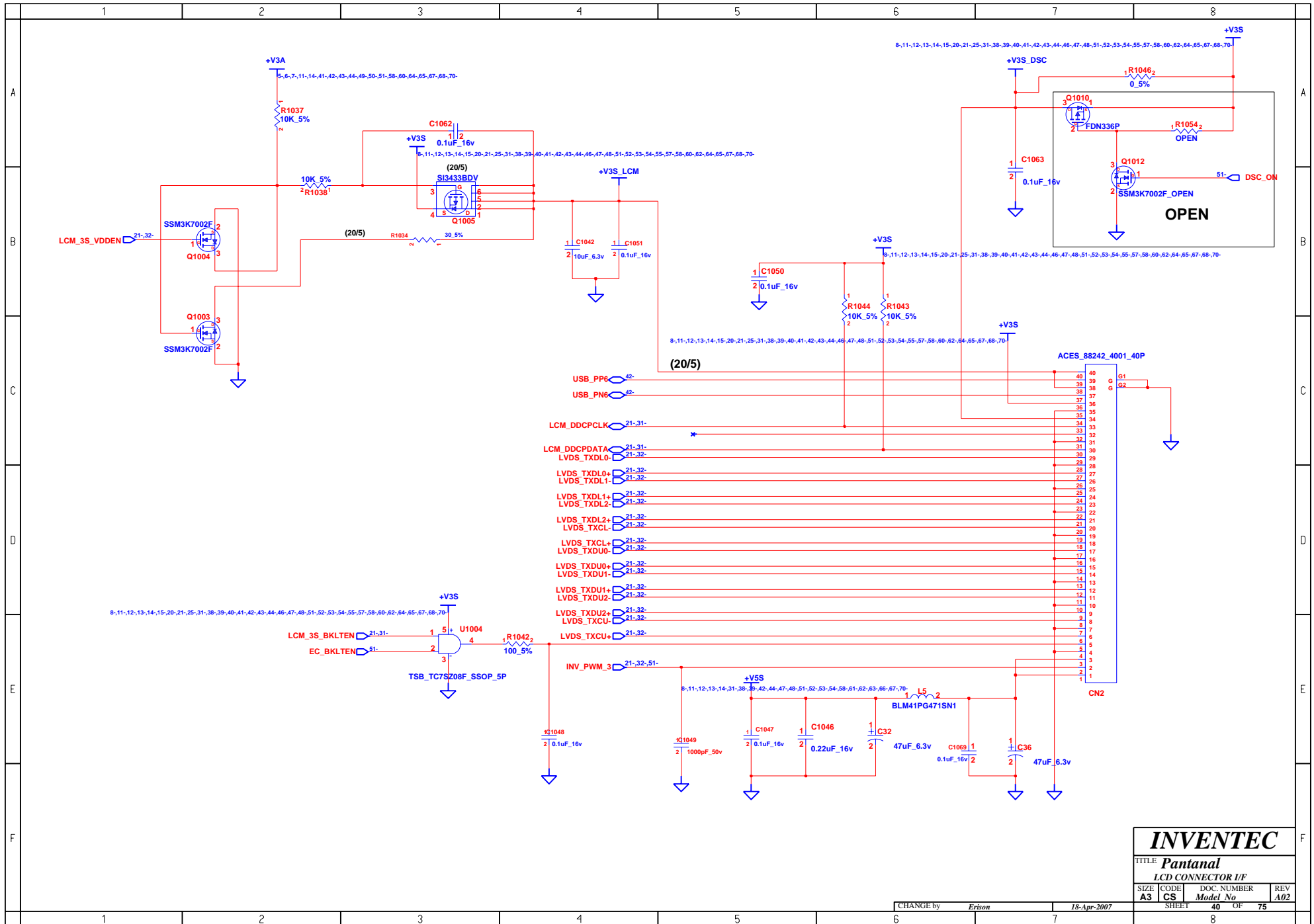
TITLE			
<b><i>Pantanal</i></b> <b><i>VRAM GDDR2-2</i></b>			
SIZE	CODE	DOC. NUMBER	REV
<b>A3</b>	<b>CS</b>	<b><i>Model No</i></b>	<b>A02</b>

CHANGE by	<i>Erison</i>	<i>18-Apr-2007</i>
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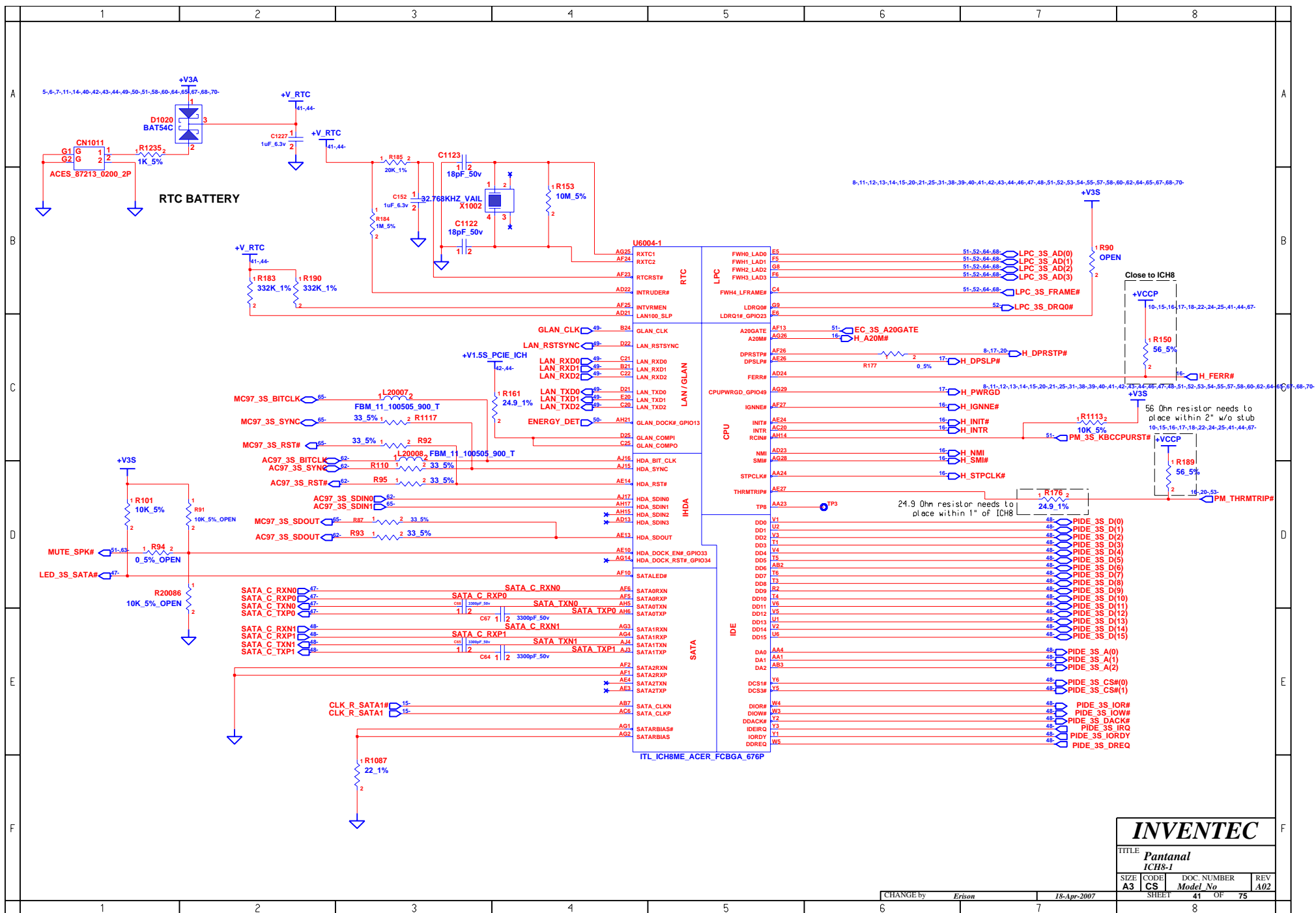
CS	Model No		
SHEET	37	OF	75



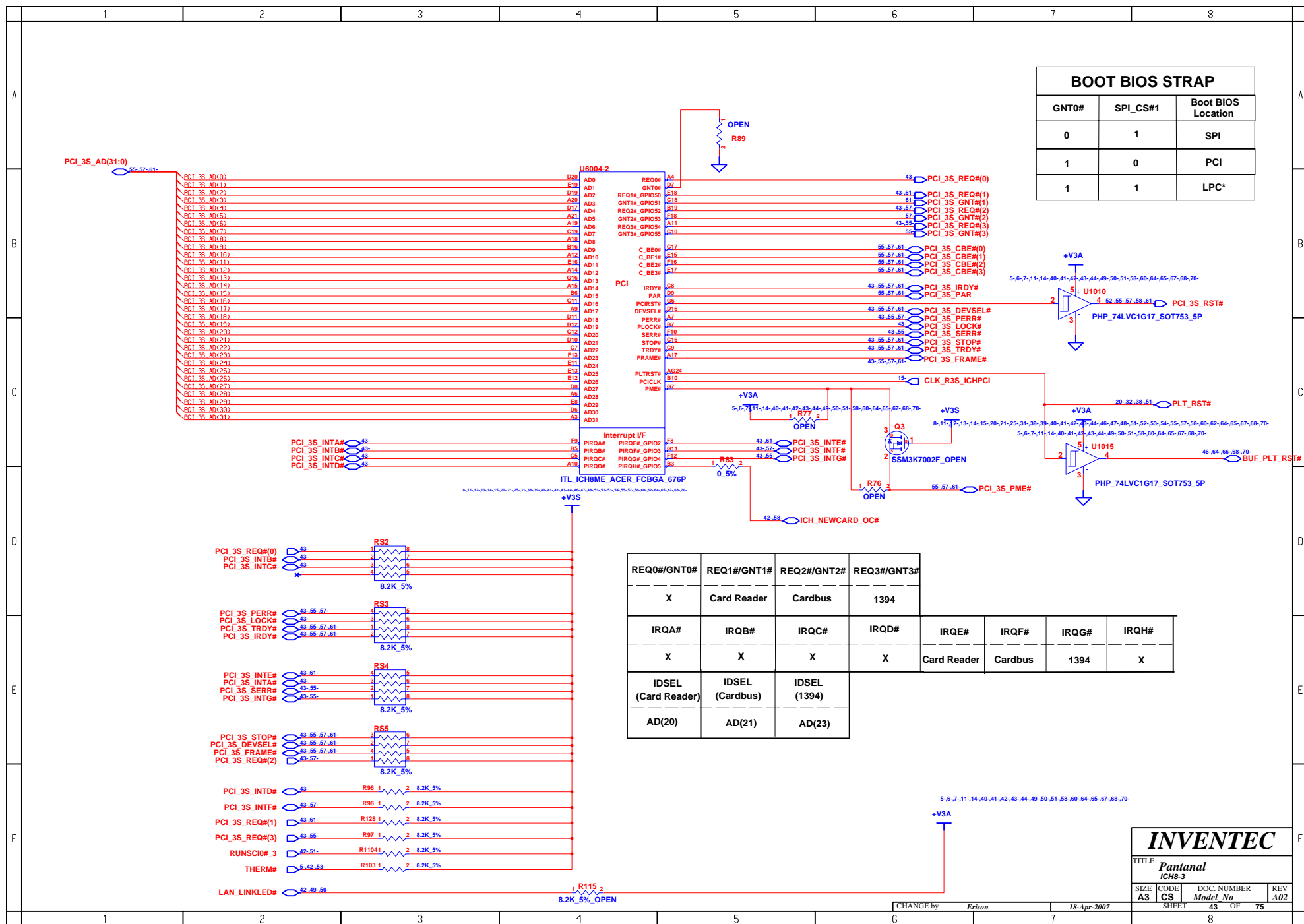














	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F

ITL\_ICH8ME\_ACER\_FCBGA\_676P

U6004-5

A23

VS0001

VS0099

K7

AA2

VS0002

VS0100

L1

AA7

VS0003

VS0101

L13

AB2

VS0004

VS0102

L15

AB1

VS0005

VS0103

L26

AB24

VS0006

VS0104

L27

AC11

VS0007

VS0105

L4

AC14

VS0008

VS0106

L5

AC25

VS0009

VS0107

M12

AC26

VS0010

VS0108

M13

AC27

VS0011

VS0109

M14

AD17

VS0012

VS0110

M15

AD20

VS0013

VS0111

M16

AD28

VS0014

VS0112

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AD29

VS0015

VS0113

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AD1

VS0016

VS0114

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VS0116

M3

AE1

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N1

AE12

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AF14

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VS0127

N27

AF18

VS0030

VS0128

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VS0131

P12

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VS0034

VS0132

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AH19

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VS0138

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VS0139

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VS0140

R11

AH26

VS0043

VS0141

R12

AH3

VS0044

VS0142

R13

AH4

VS0045

VS0143

R14

AH8

VS0046

VS0144

R15

AJ5

VS0047

VS0145

R16

B11

VS0048

VS0146

R17

B14

VS0049

VS0147

R18

B17

VS0050

VS0148

R28

B2

VS0051

VS0149

R4

B20

VS0052

VS0150

T12

B22

VS0053

VS0151

T13

B8

VS0054

VS0152

T14

C24

VS0055

VS0153

T15

C26

VS0056

VS0154

T16

C27

VS0057

VS0155

T17

C6

VS0058

VS0156

T2

D12

VS0059

VS0157

U12

D15

VS0060

VS0158

U13

D18

VS0061

VS0159

U14

D2

VS0062

VS0160

U15

D4

VS0063

VS0161

U16

E21

VS0064

VS0162

U17

E24

VS0065

VS0163

U23

F4

VS0066

VS0164

U26

E9

VS0067

VS0165

U27

F15

VS0068

VS0166

U3

F24

VS0069

VS0167

U5

F28

VS0070

VS0168

V13

F29

VS0071

VS0169

V15

F7

VS0072

VS0170

V28

G1

VS0073

VS0171

V29

G10

VS0074

VS0172

W2

G13

VS0075

VS0173

W26

G19

VS0076

VS0174

W27

G23

VS0077

VS0175

Y28

G25

VS0078

VS0176

Y29

G27

VS0079

VS0177

Y4

H25

VS0080

VS0178

AB4

H28

VS0081

VS0179

AB23

H29

VS0082

VS0180

AB5

I1

VS0083

VS0181

AB6

I2

VS0084

VS0182

AD5

I6

VS0085

VS0183

JA

J1

VS0086

VS0184

W24

J25

VS0087

VSS\_NCTF\_01

A1

J26

VS0088

VSS\_NCTF\_02

A2

J27

VS0089

VSS\_NCTF\_03

A28

J4

VS0090

VSS\_NCTF\_04

A29

J5

VS0091

VSS\_NCTF\_05

AH1

K21

VS0092

VSS\_NCTF\_06

AH29

K28

VS0093

VSS\_NCTF\_07

AJ1

K29

VS0094

VSS\_NCTF\_08

AJ2

K3

VS0095

VSS\_NCTF\_09

AJ28

K6

VS0096

VSS\_NCTF\_010

AJ29

K8

VS0097

VSS\_NCTF\_011

B1

K9

VS0098

VSS\_NCTF\_012

B29

CHANGE by

Erison

18-Apr-2007

SIZE

A3

CODE

CS

DOC. NUMBER

Model No

SHEET

45

OF

75

REV

A02

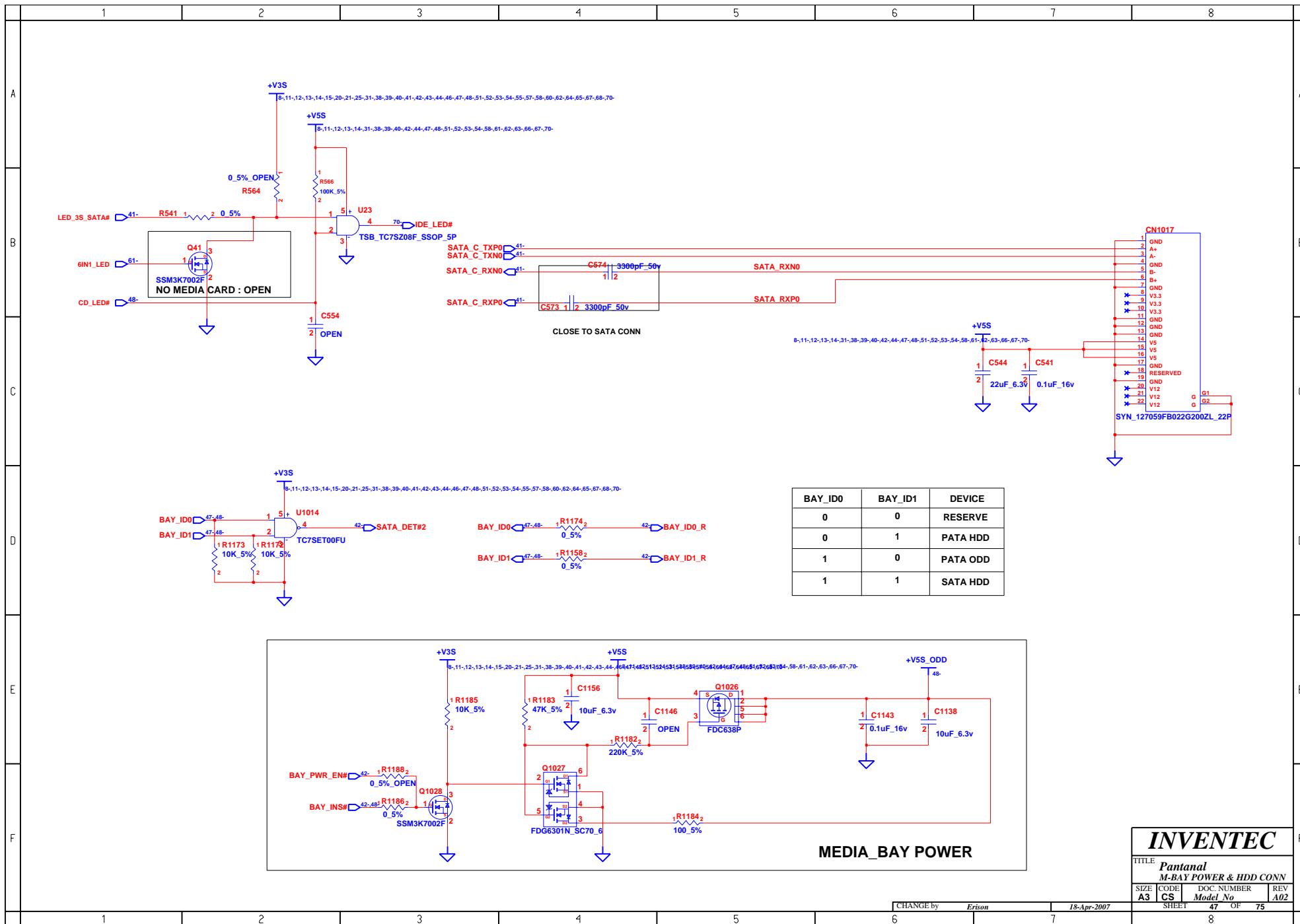
INVENTEC

TITLE

Pantanal

ICH8-5





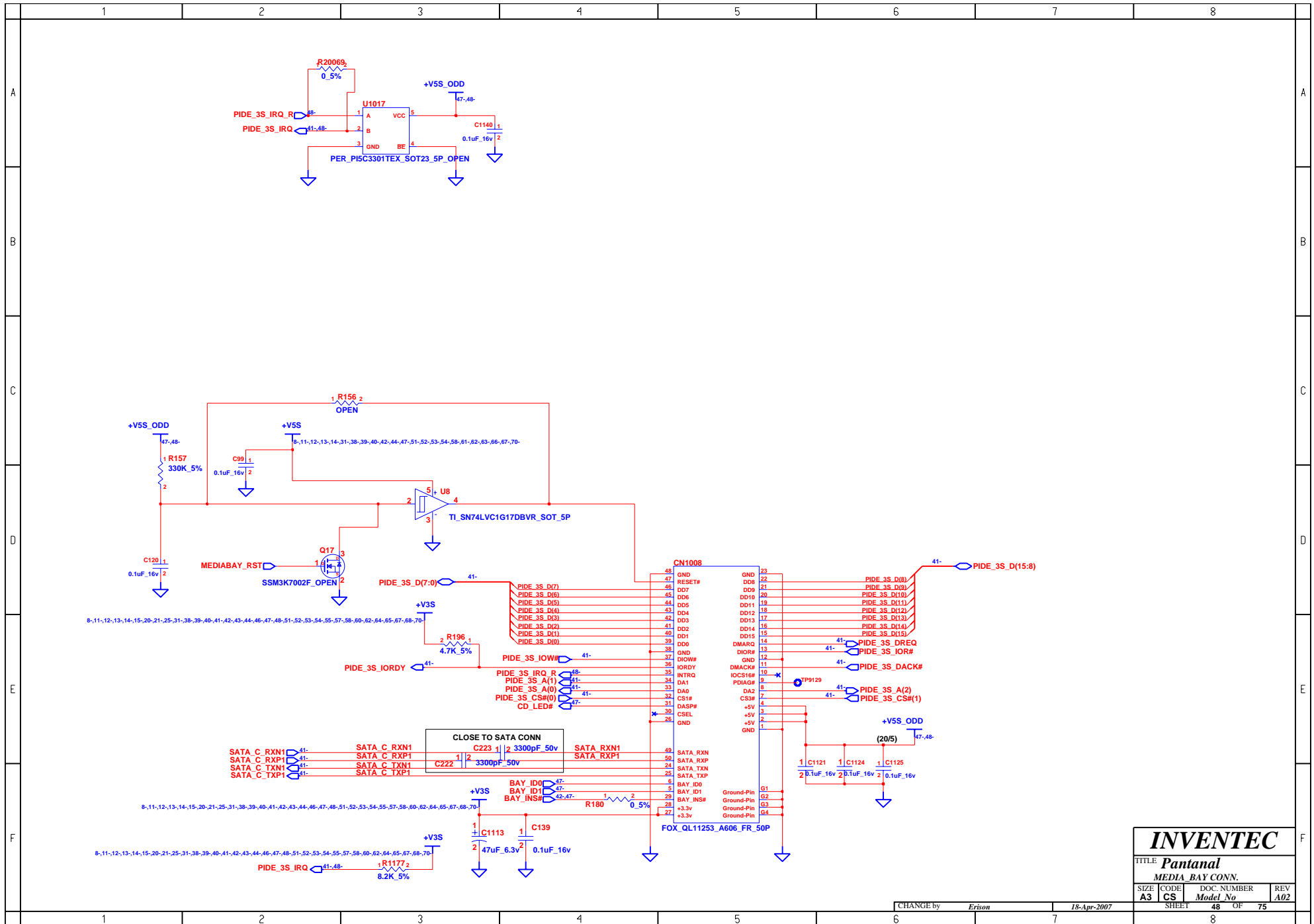
**INVENTEC**

TITLE: **Pantanal**  
**M-BAY POWER & HDD CONN**

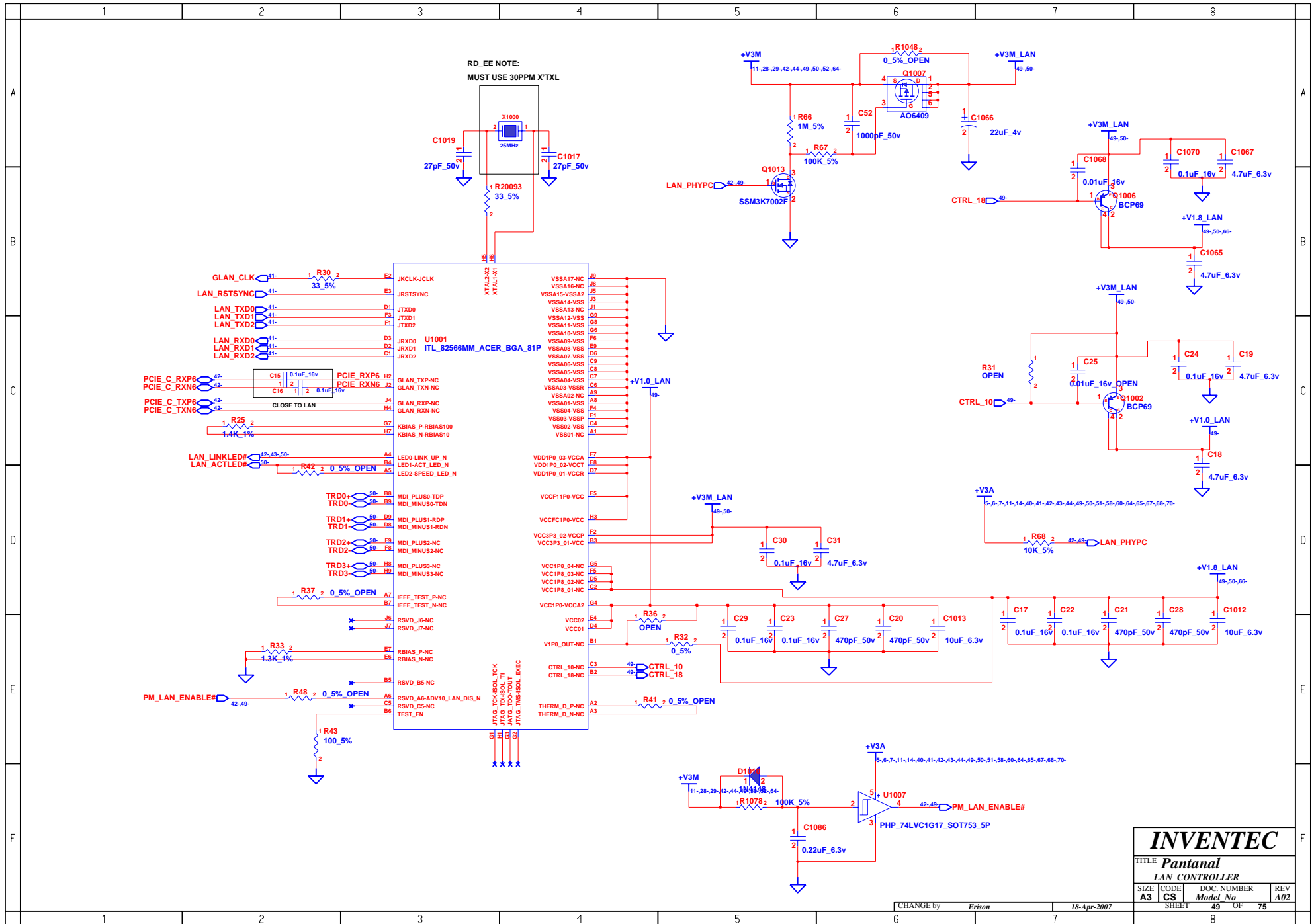
SIZE: **A3** CODE: **CS** DOC. NUMBER: **Model No** REV: **A02**

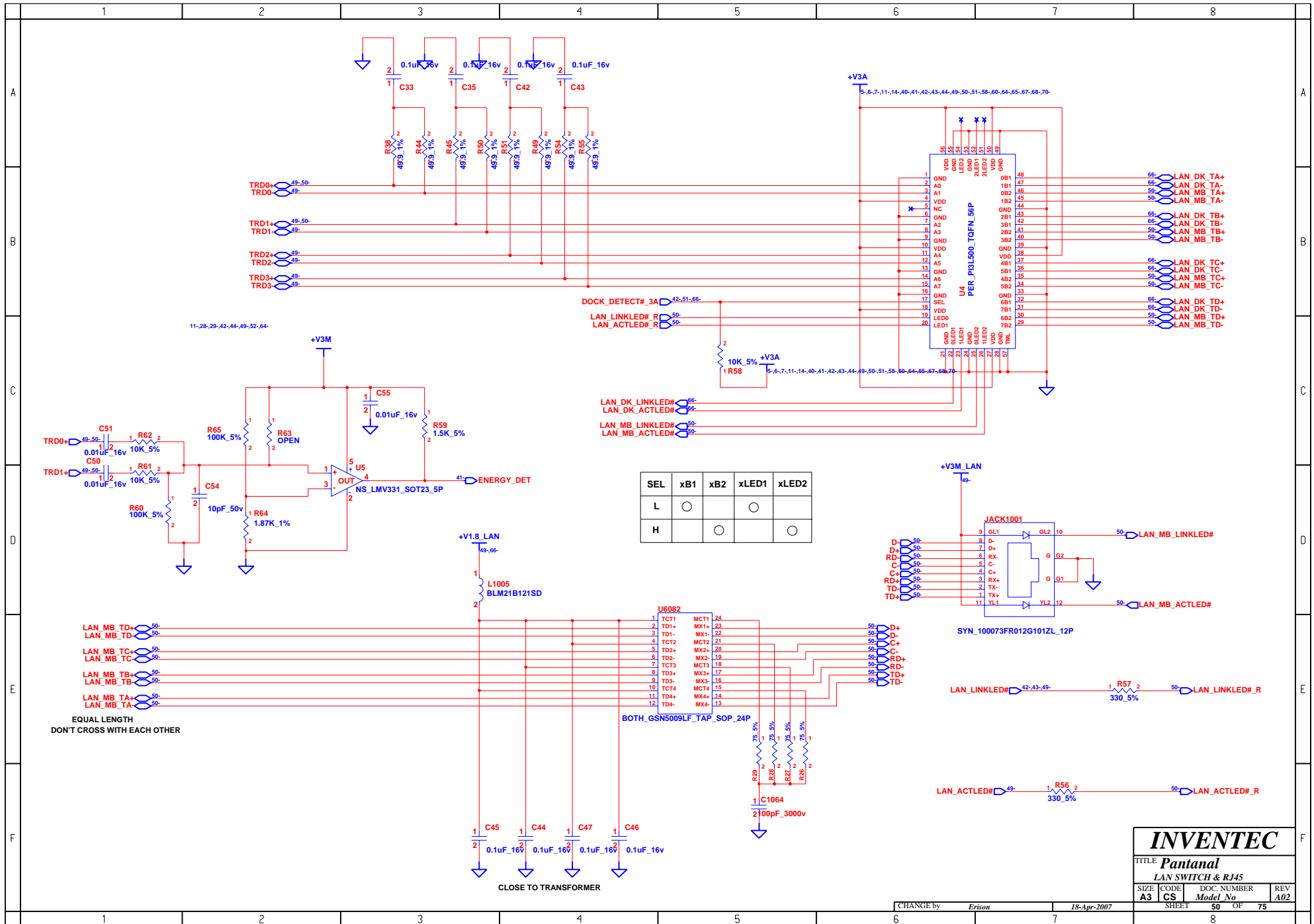
CHANGE by: **Erison** 18-Apr-2007

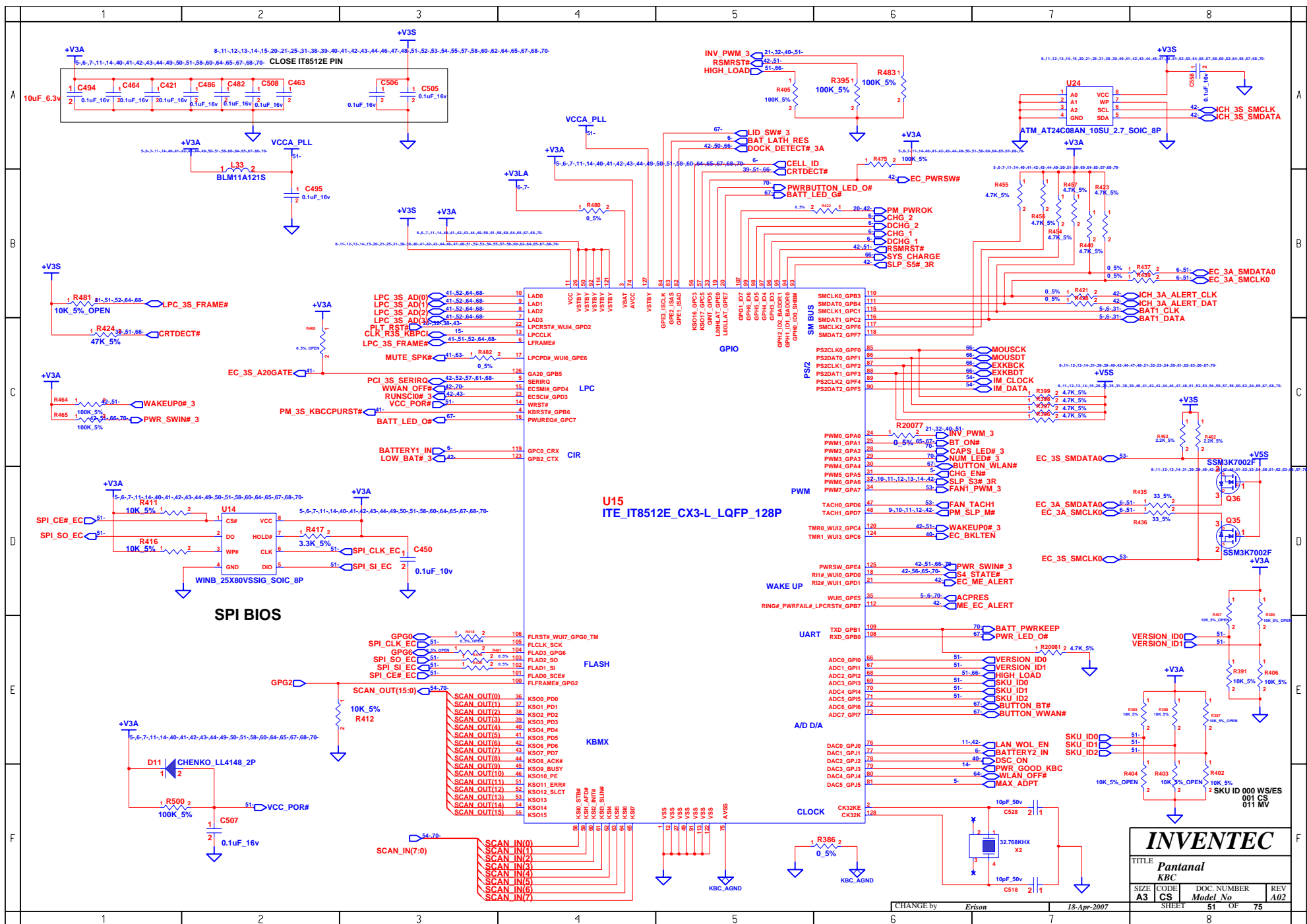
SHEET **47** OF **75**



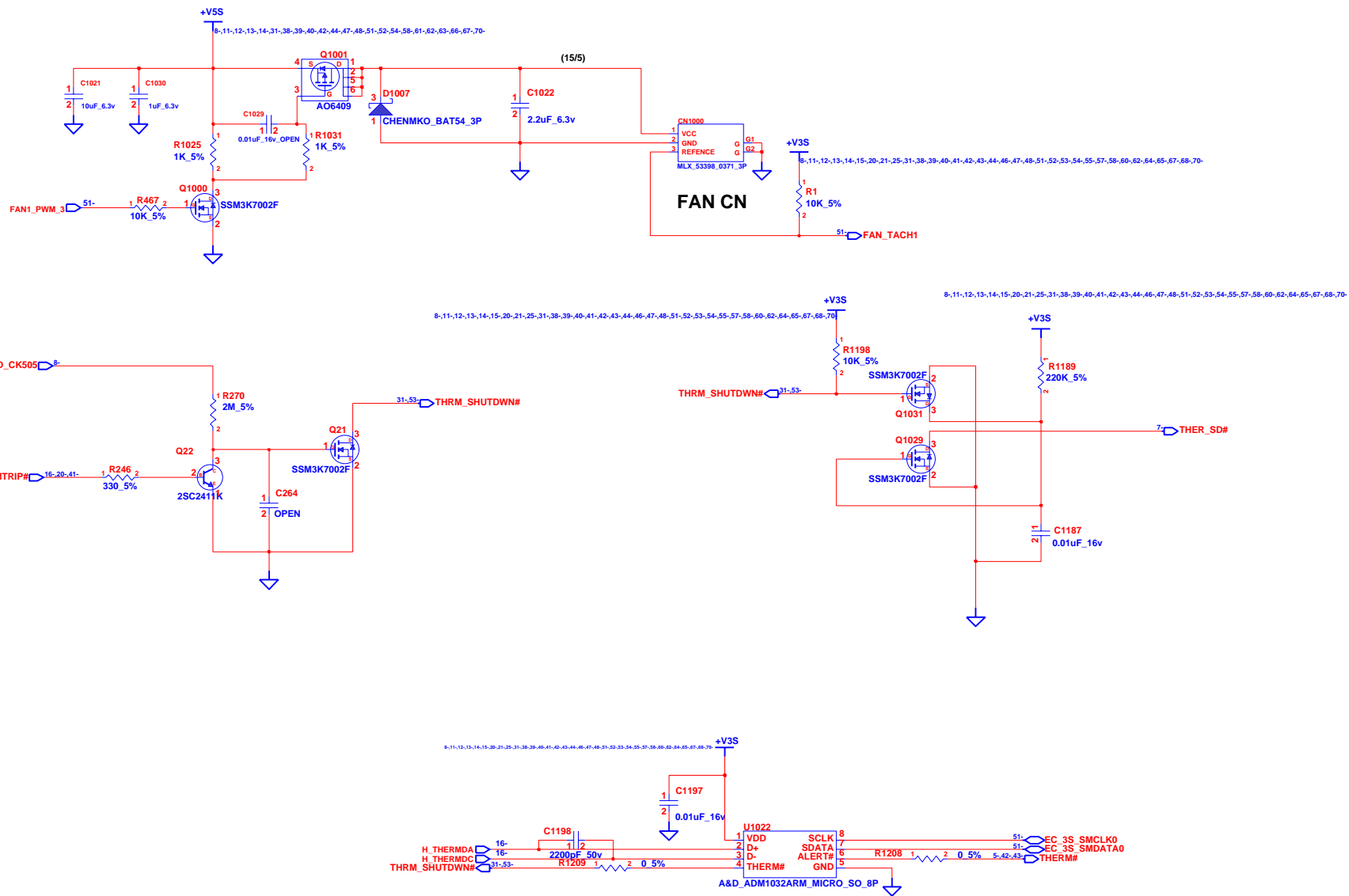










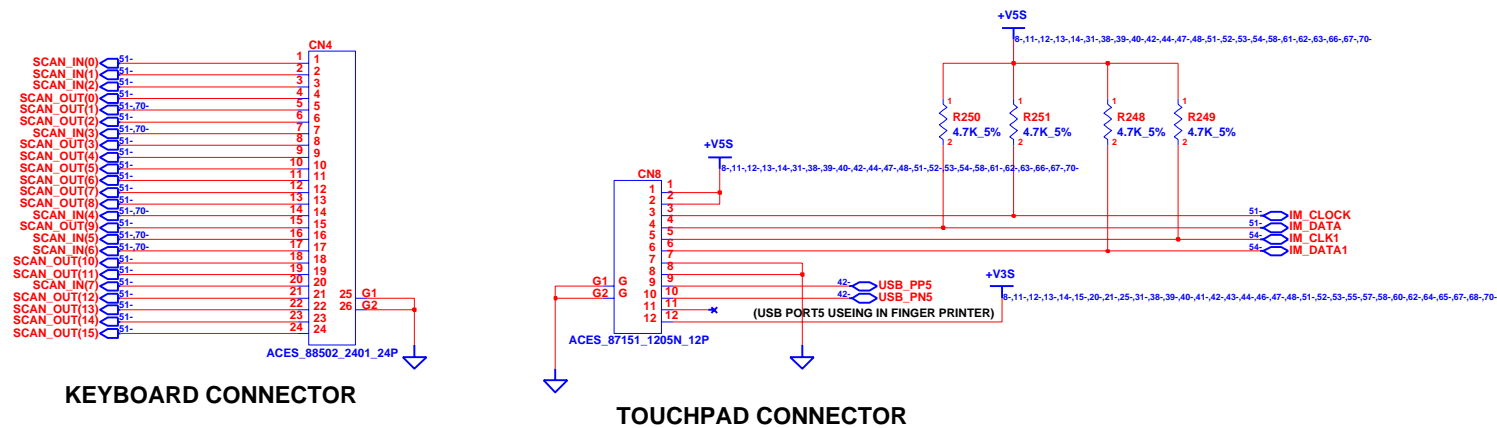


# THERMAL SENSOR FOR CPU.

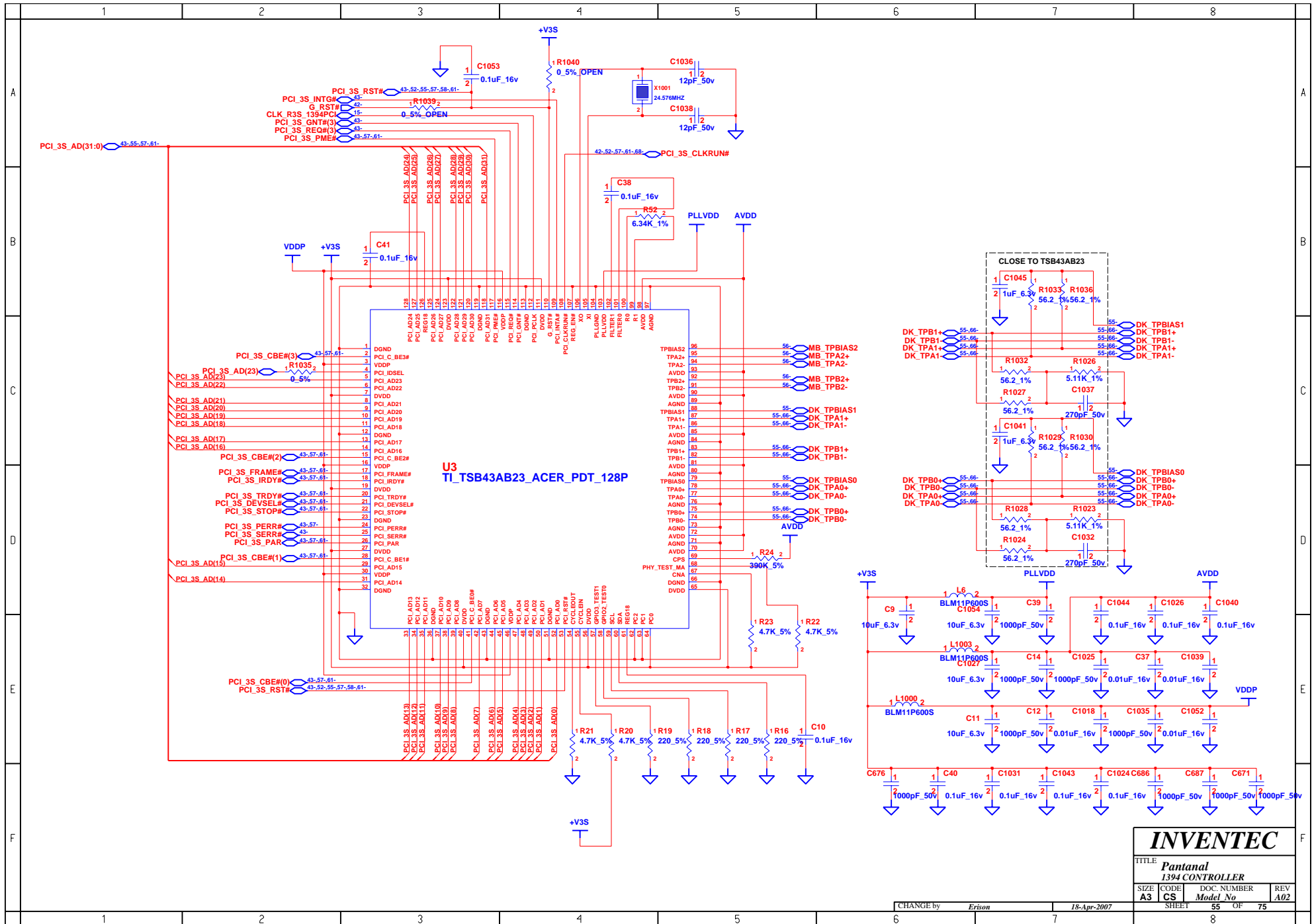
INVENTEC			
TITLE Pantanal			
THERMAL SENSOR & FAN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02

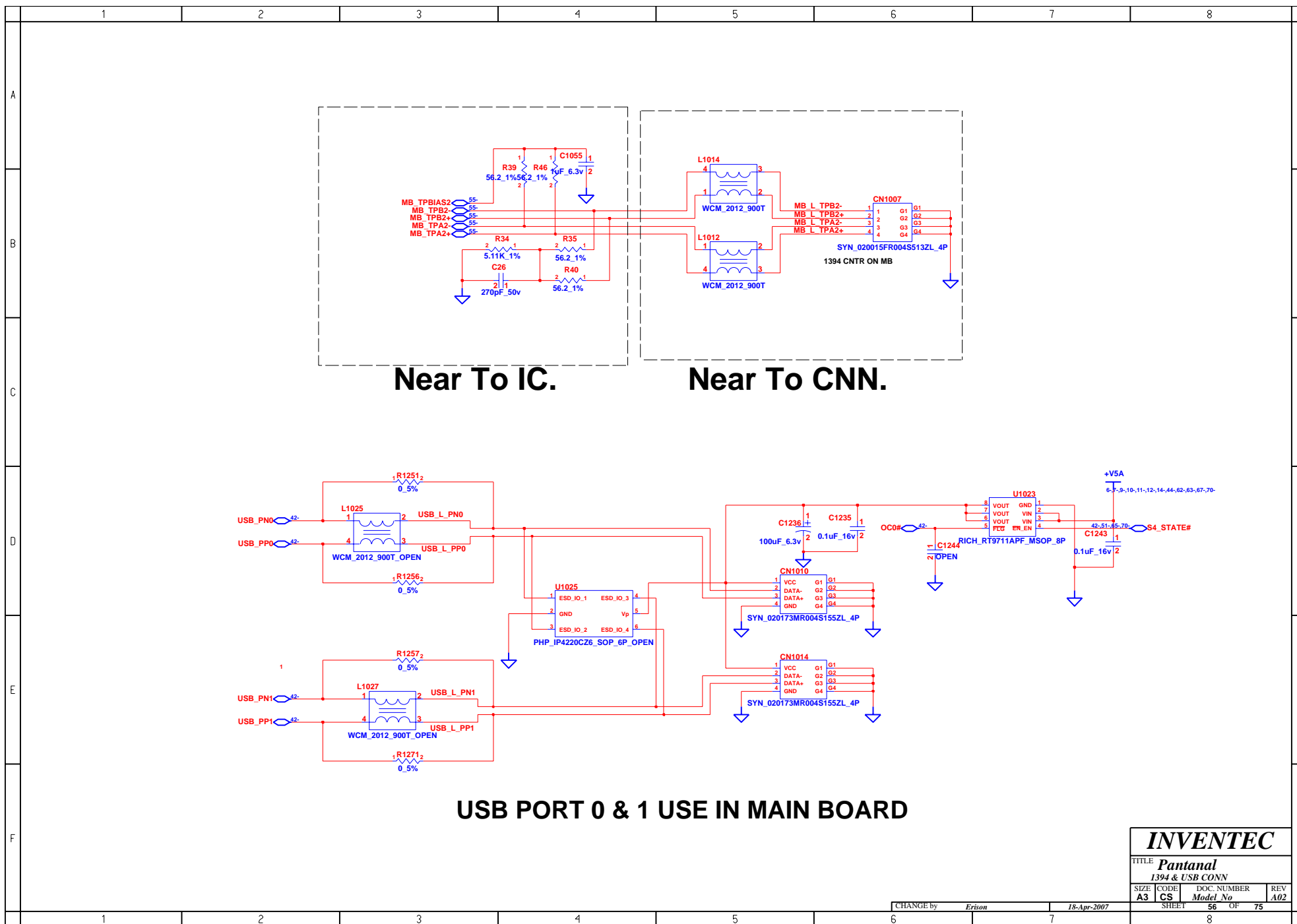
CHANGE by Erison 18-Apr-2007

SHEET 53 OF 75

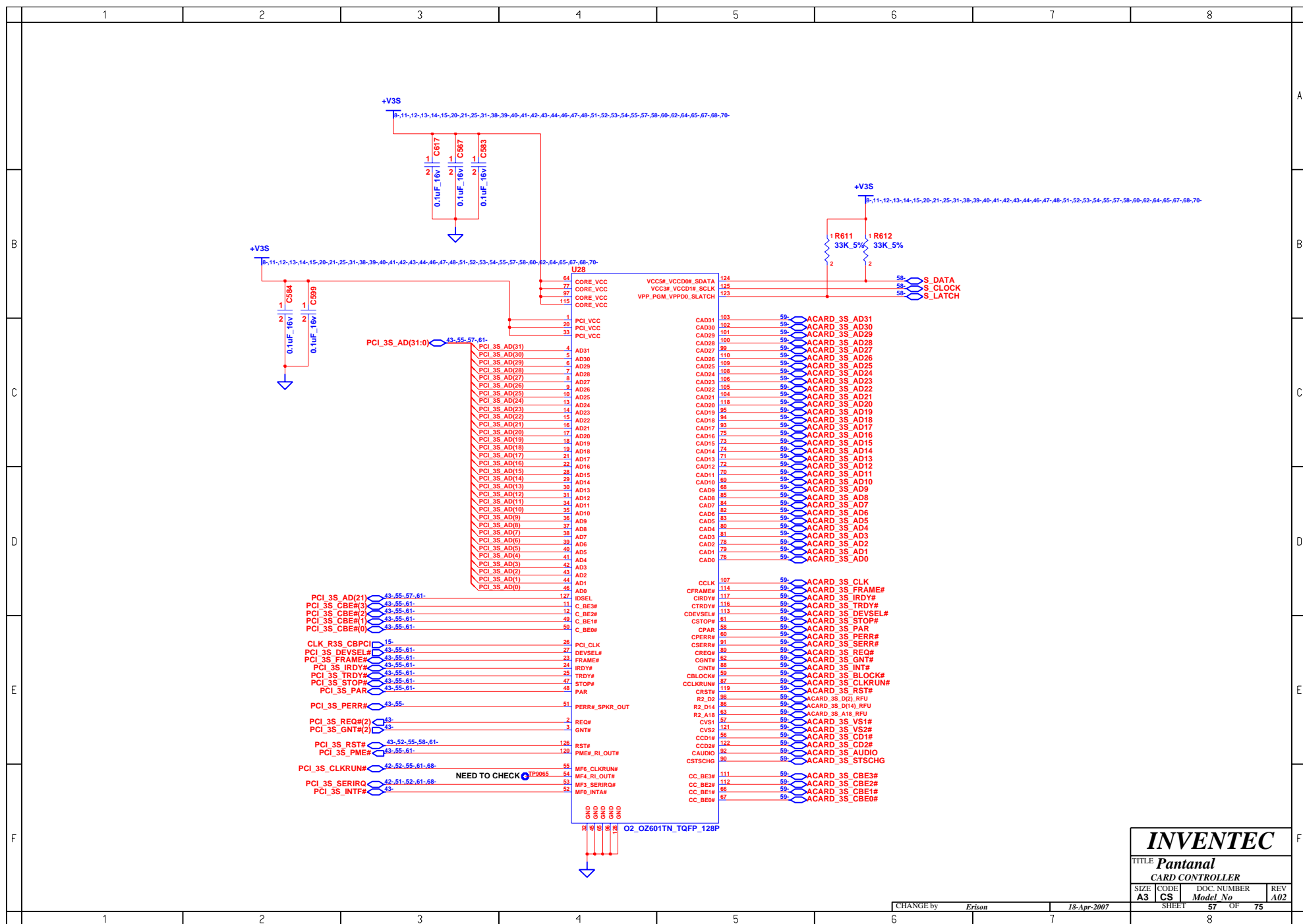


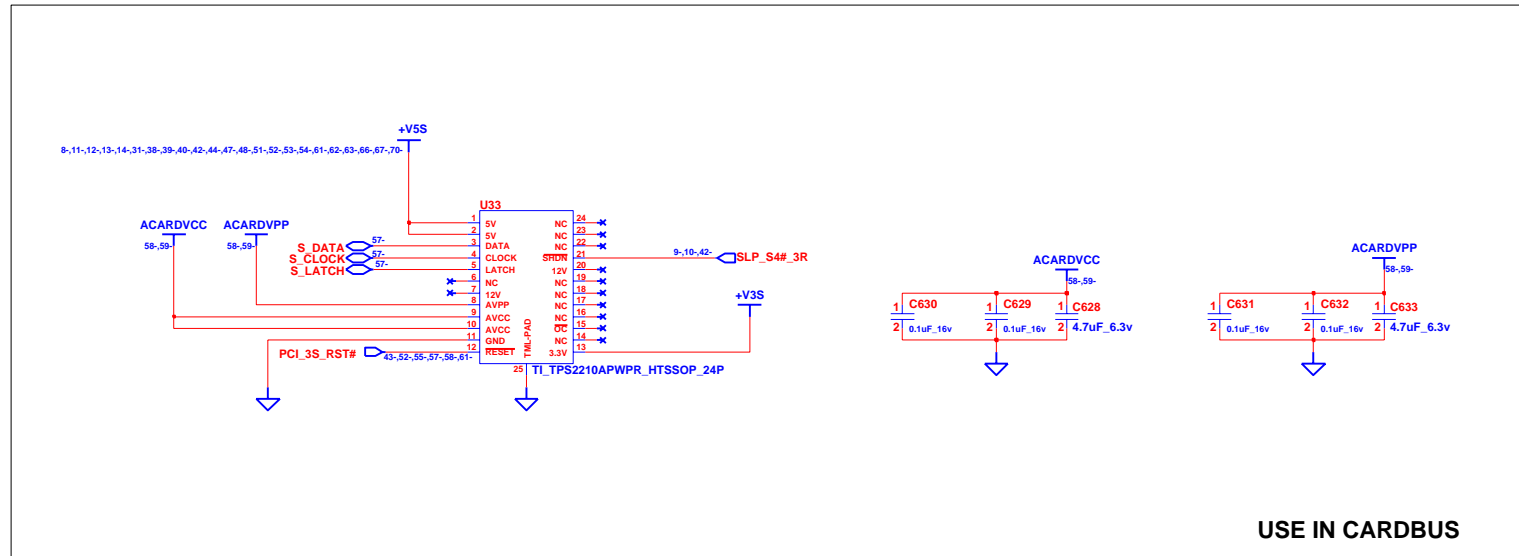
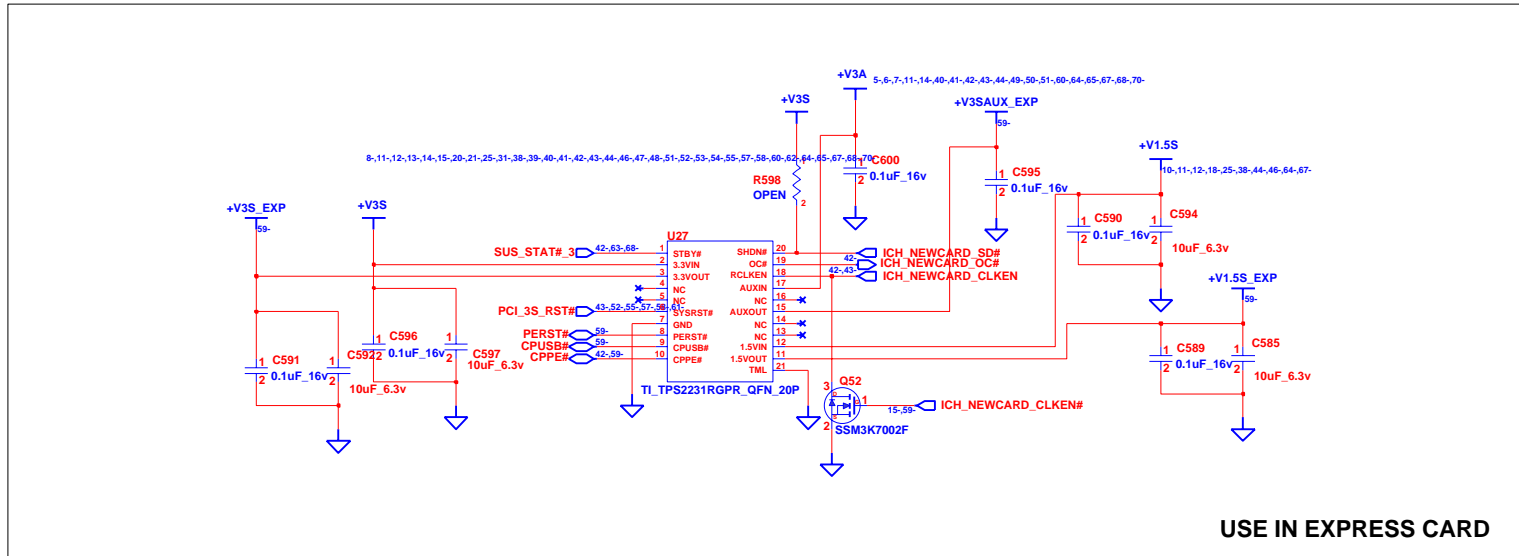
INVENTEC			
TITLE <i>Pantanal</i>			
KB, TP & SP I/F			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <i>Model No</i>	REV <b>A02</b>
CHANGE by <i>Erison</i>		18-Apr-2007	
SHEET		54 OF 75	
1		8	







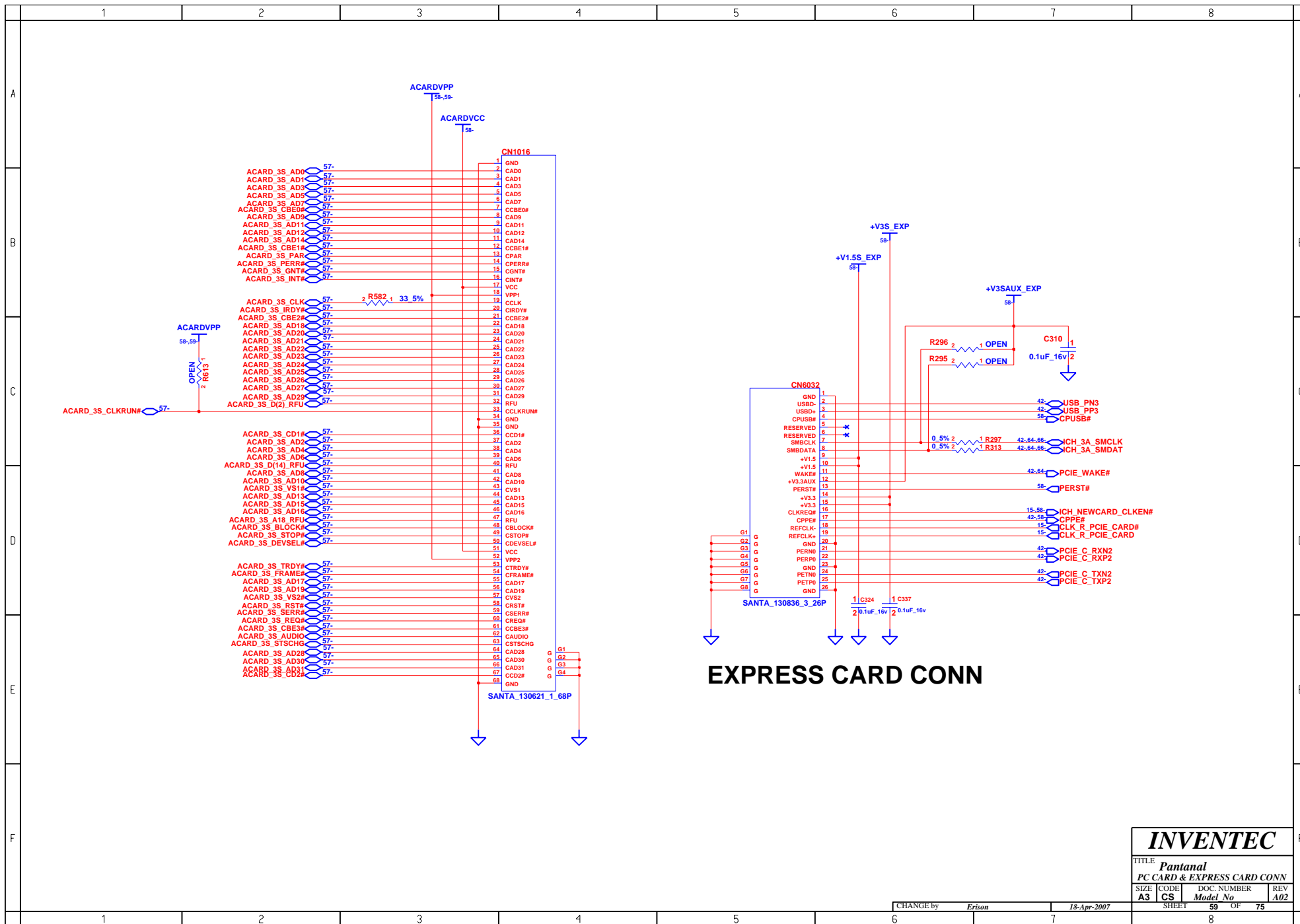


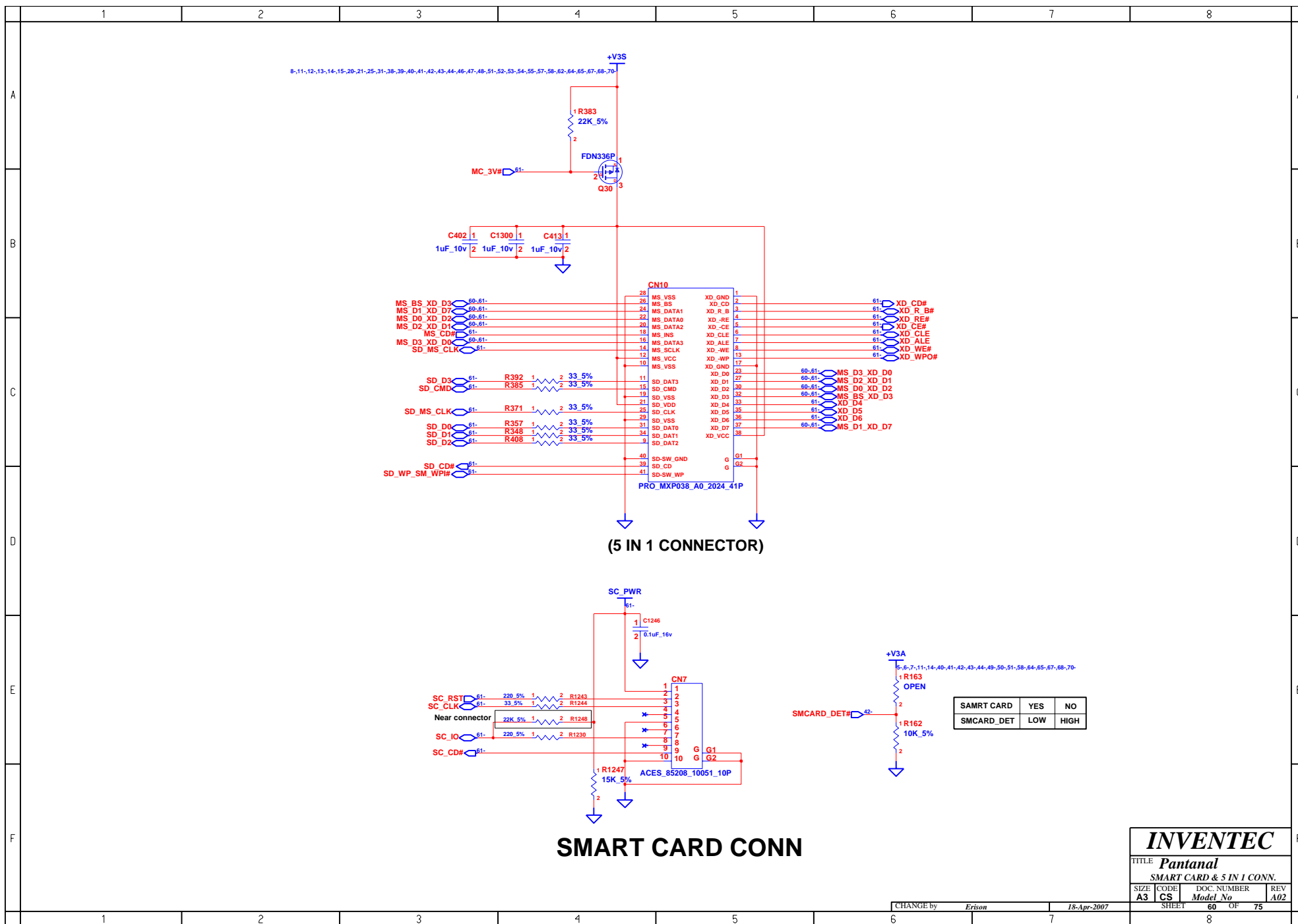


INVENTEC			
TITLE <i>Pantanal</i>			
PC CARD & EXPRESS CARD POWER			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02

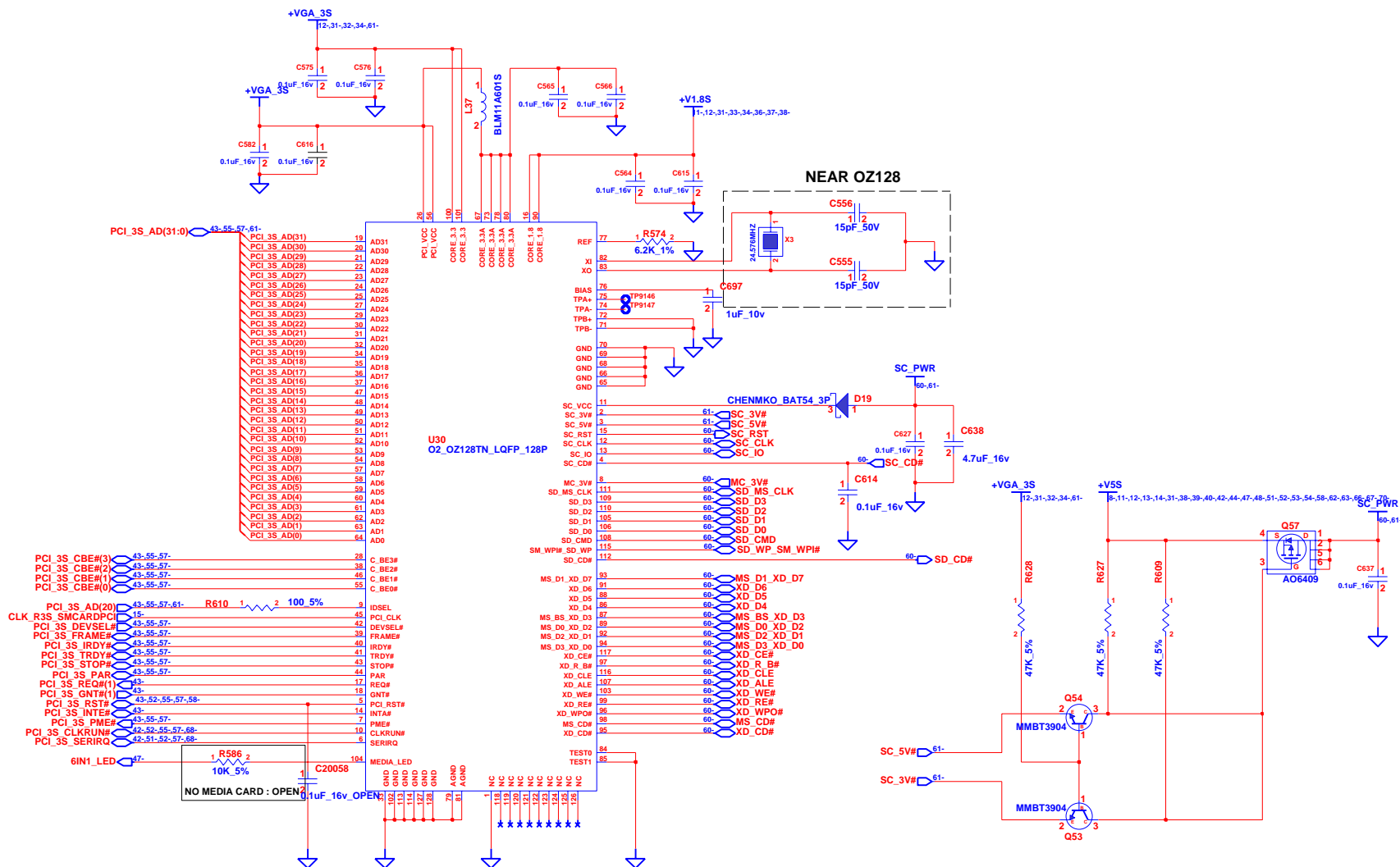
CHANGE by *Erison* 18-Apr-2007

SHEET 58 OF 75





NOTE: +V1.8S NEED POWER ON FASTER THAN +VGA\_3S



**INVENTEC**

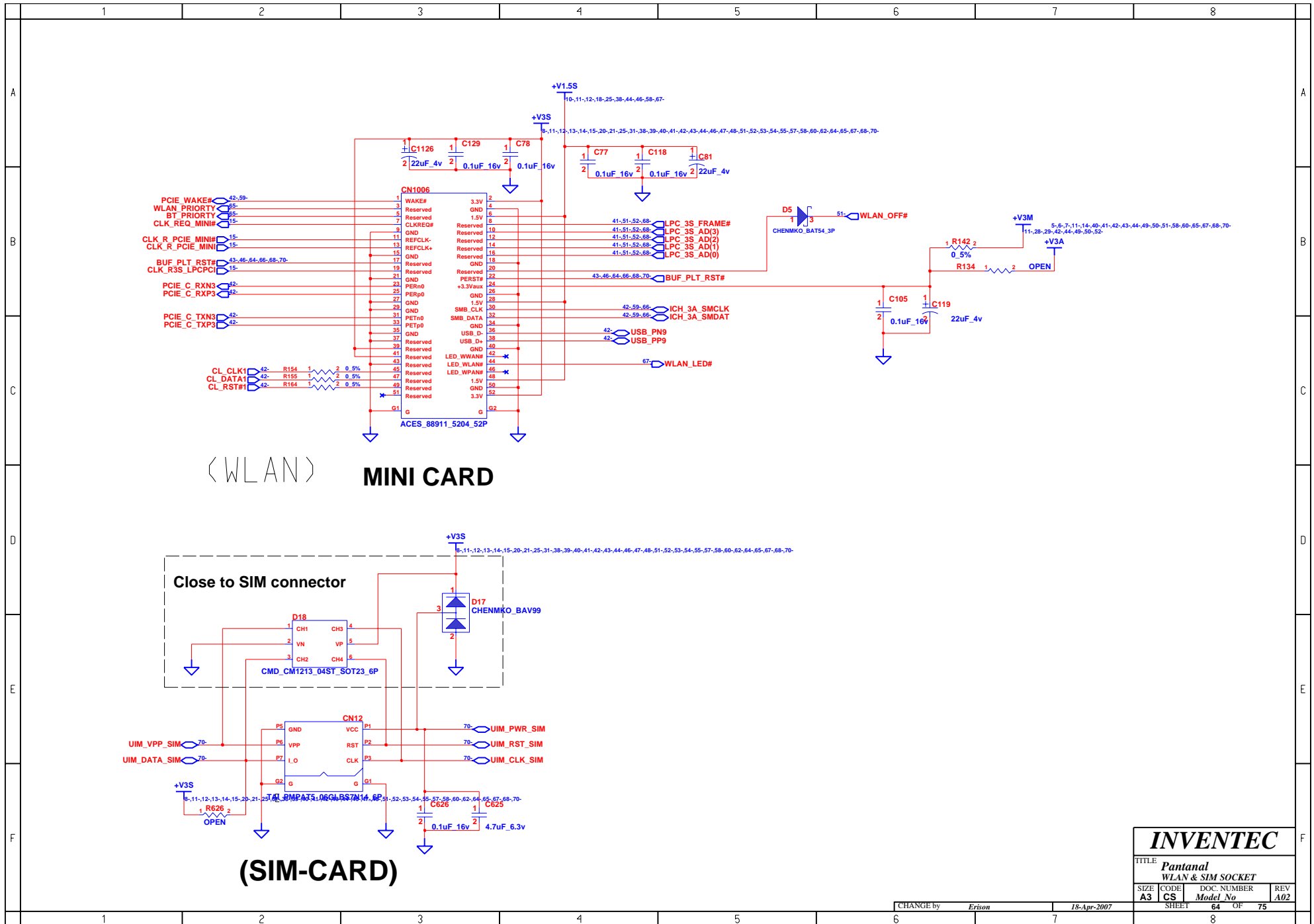
TITLE  
Pantanal  
SMART CARD IC & SC POWER  
SIZE CODE DOC. NUMBER REV  
A3 CS Model No A02

CHANGE by Erison 18-Apr-2007

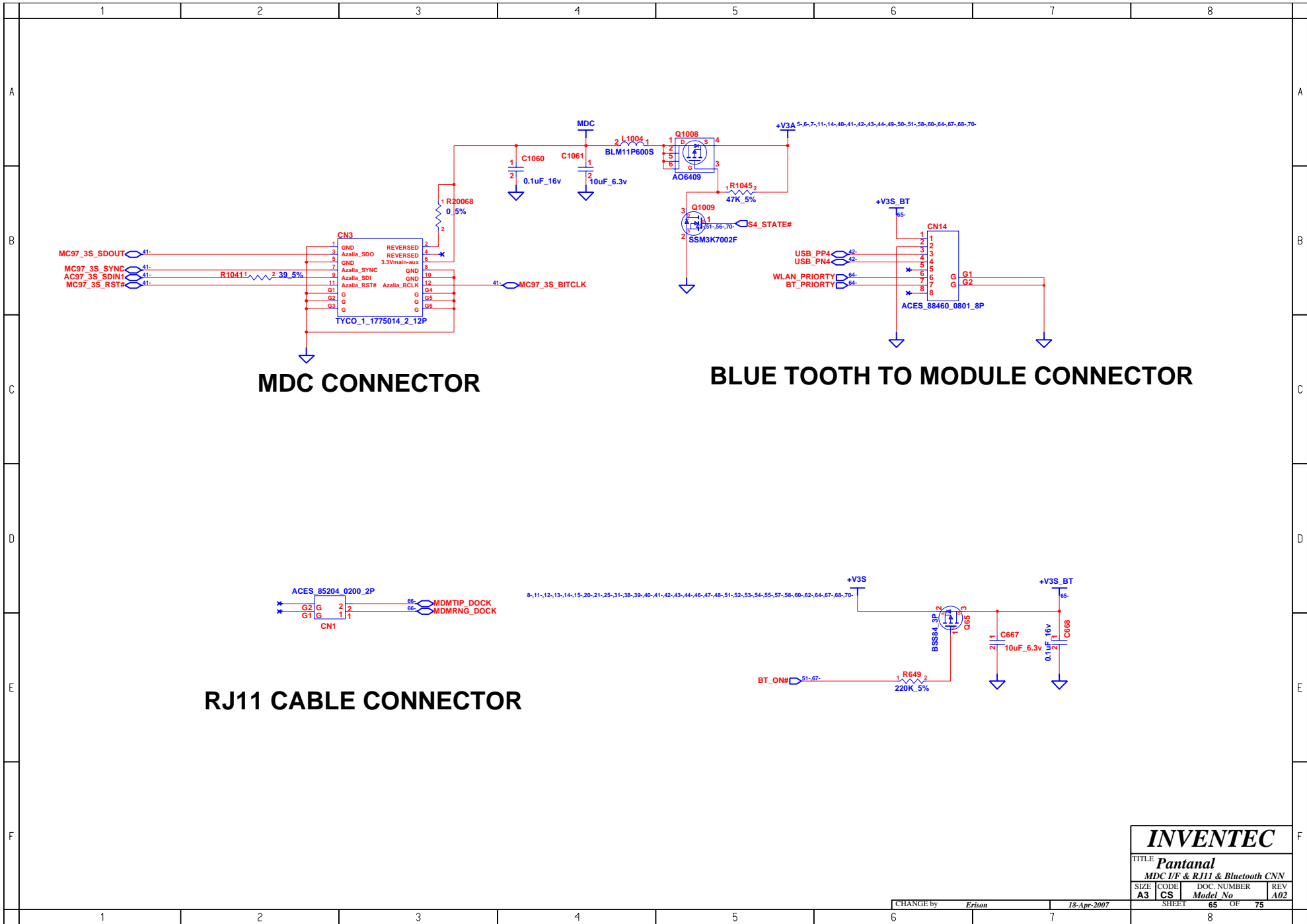
SHEET 61 OF 75



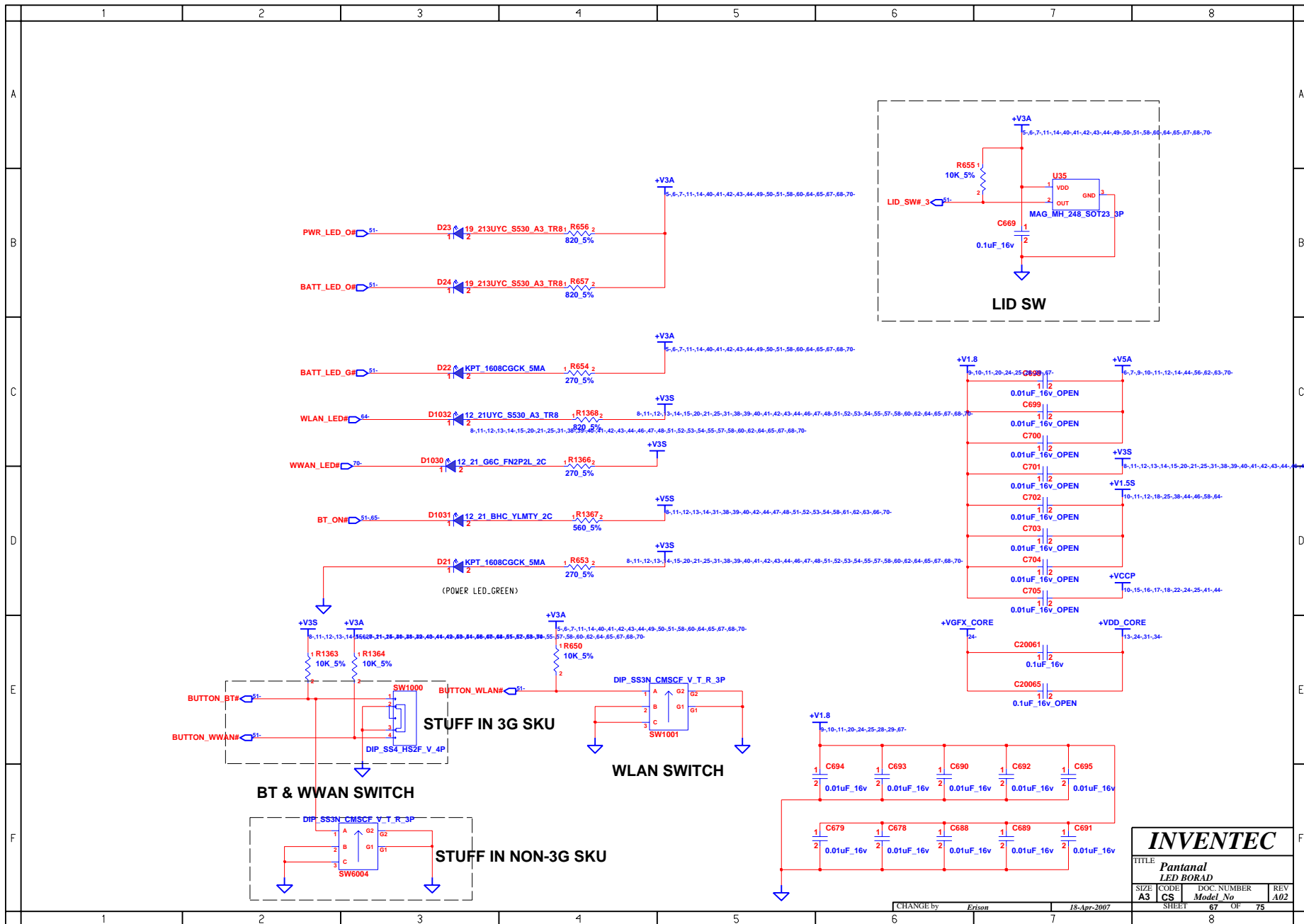






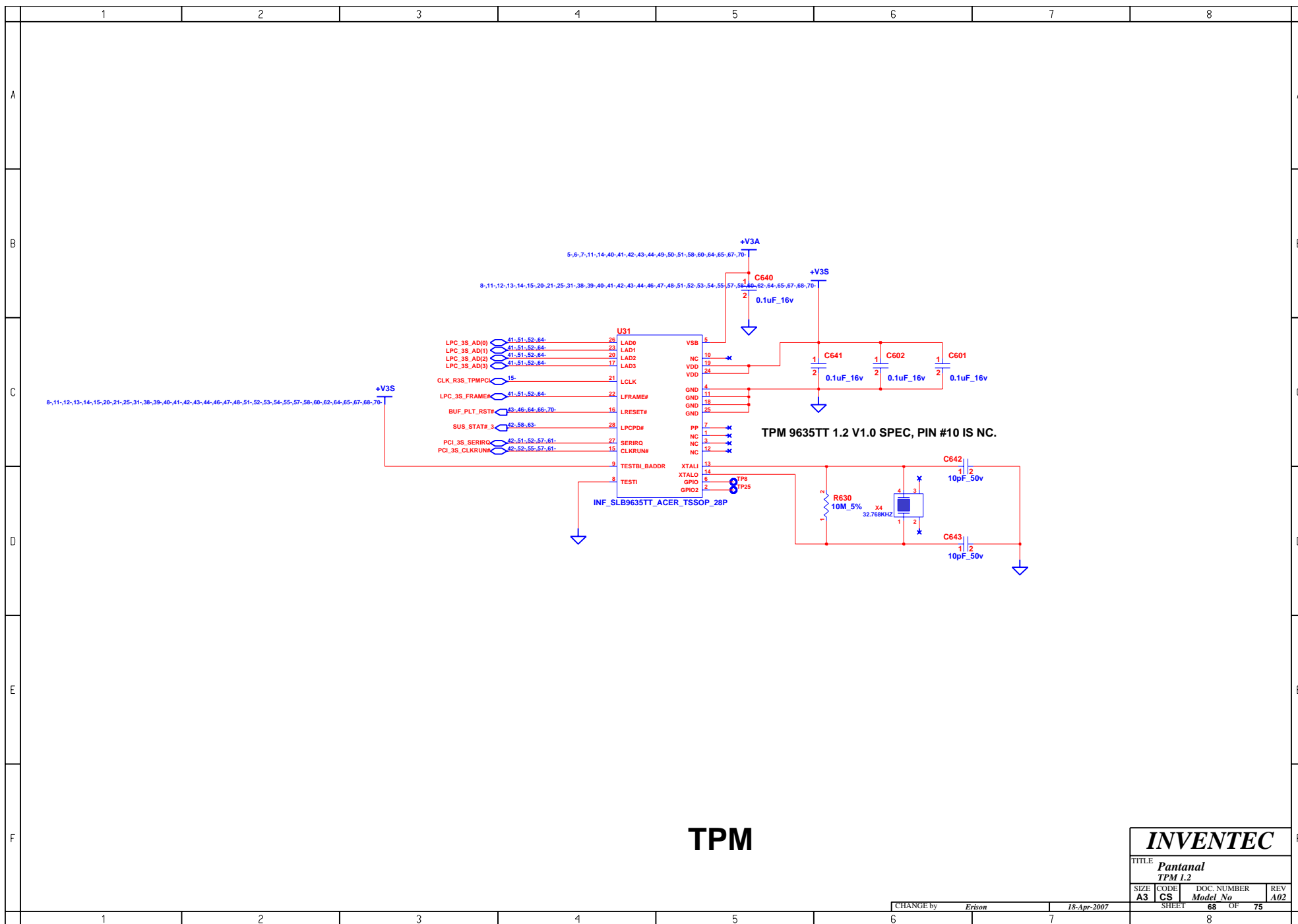




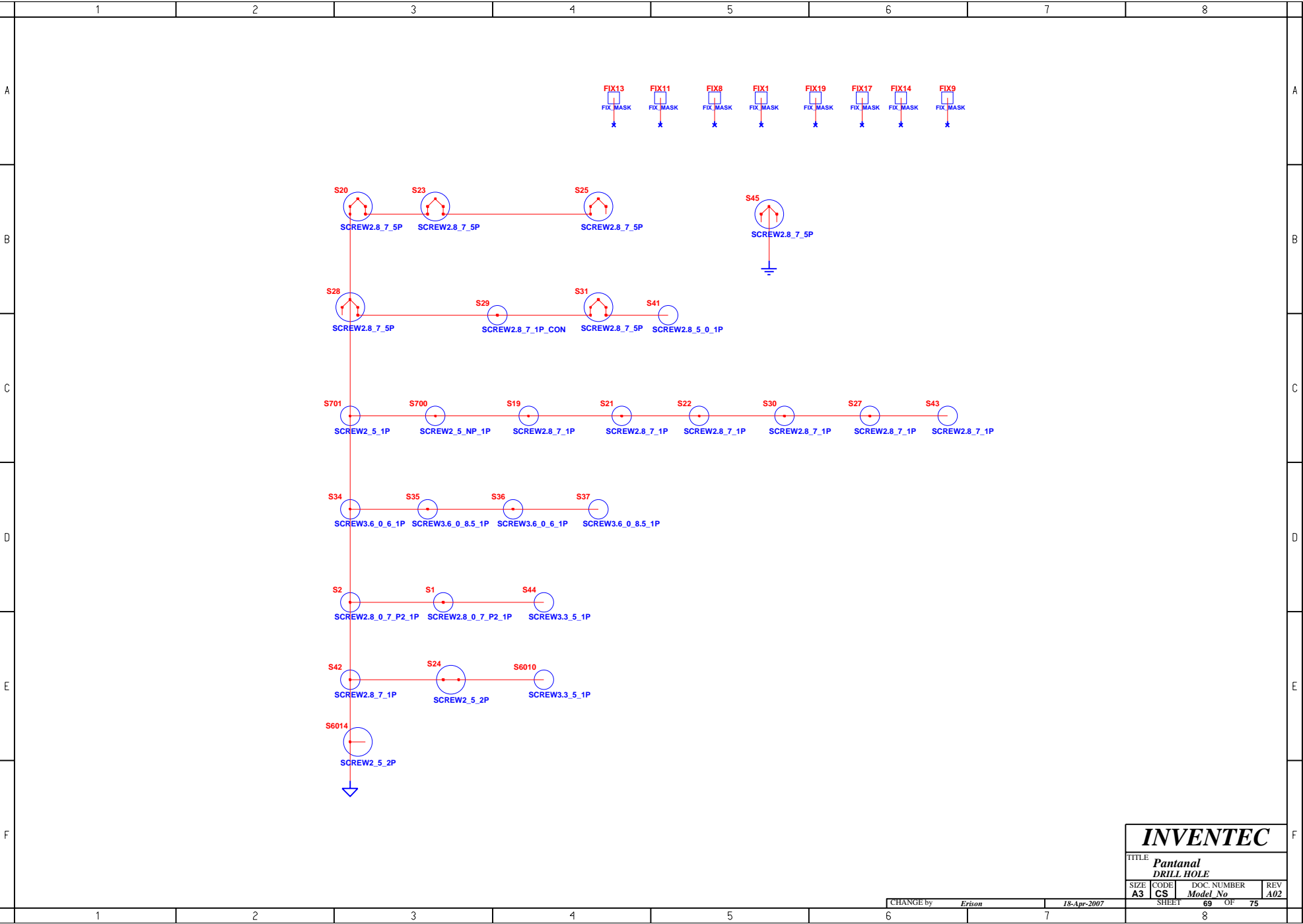


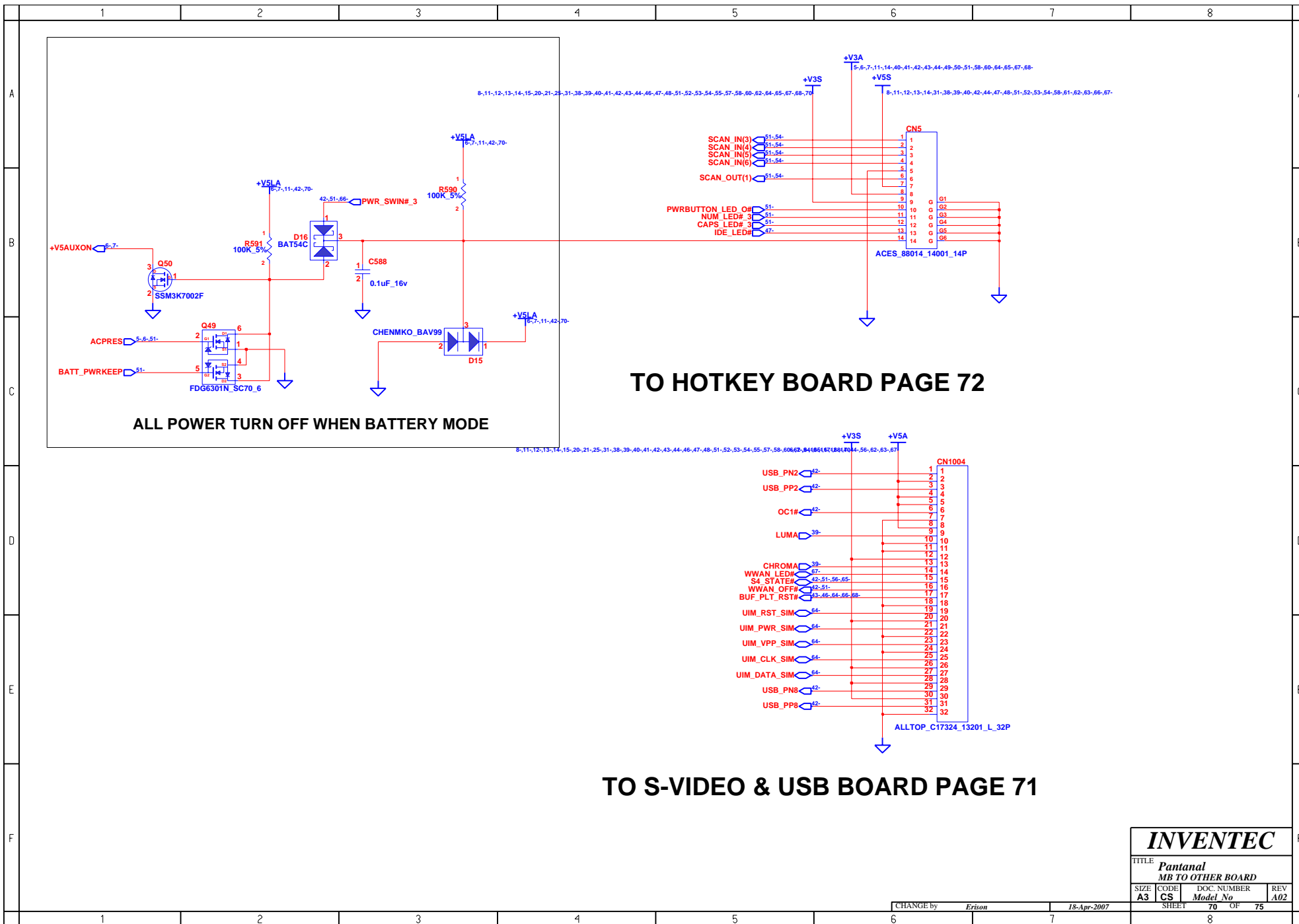
<b>INVENTEC</b>			
TITLE <i>Pantanal LED BORAD</i>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <i>Model No</i>	REV <i>A02</i>
SHEET 67		OF 75	
8			

CHANGE by Erlon 18-Apr-2007



<b>INVENTEC</b>			
TITLE <b>Pantanal</b> <b>TPM 1.2</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>Model No</b>	REV <b>A02</b>
CHANGE by <i>Erison</i>		18-Apr-2007	
SHEET <b>68</b>		OF <b>75</b>	



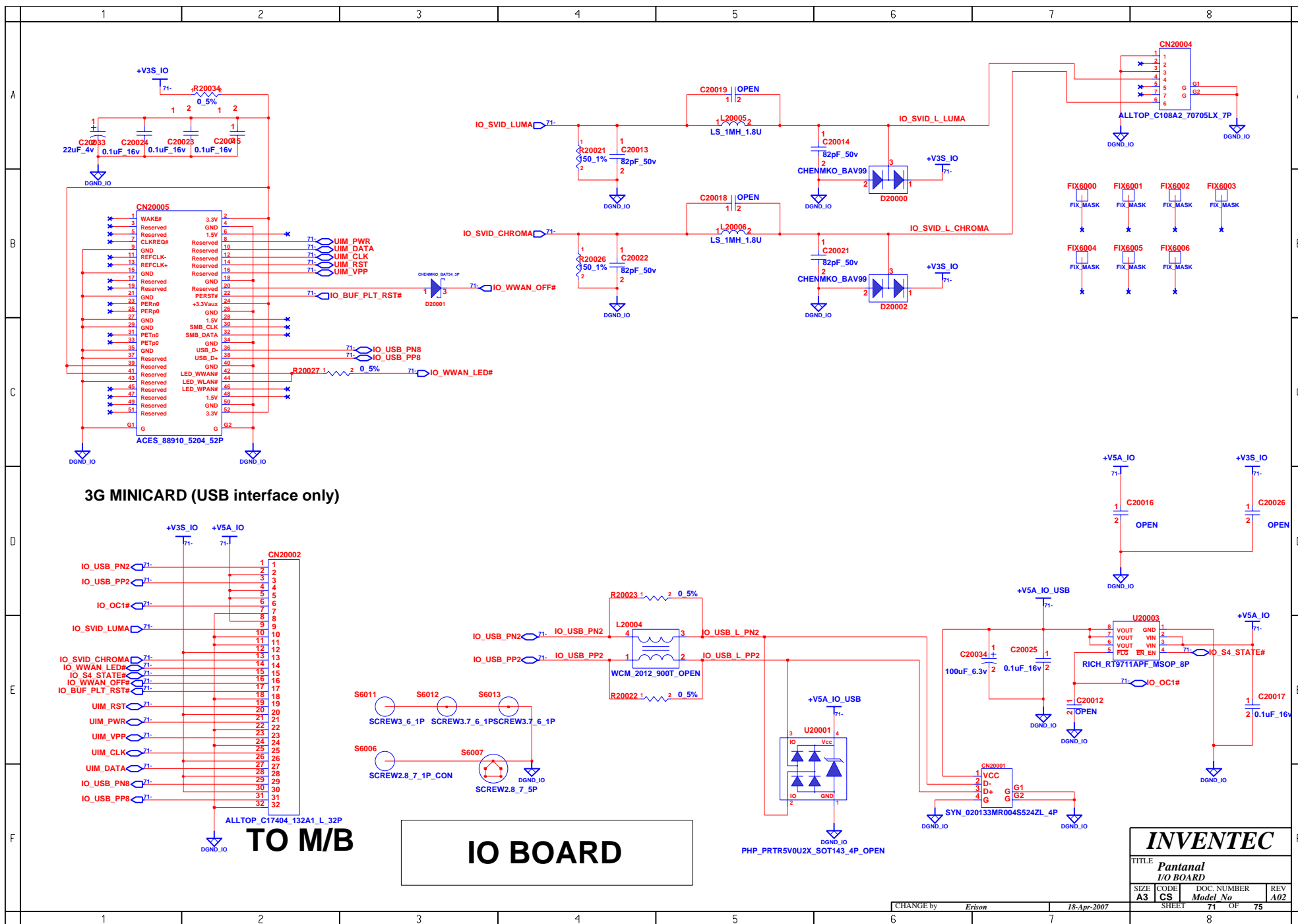


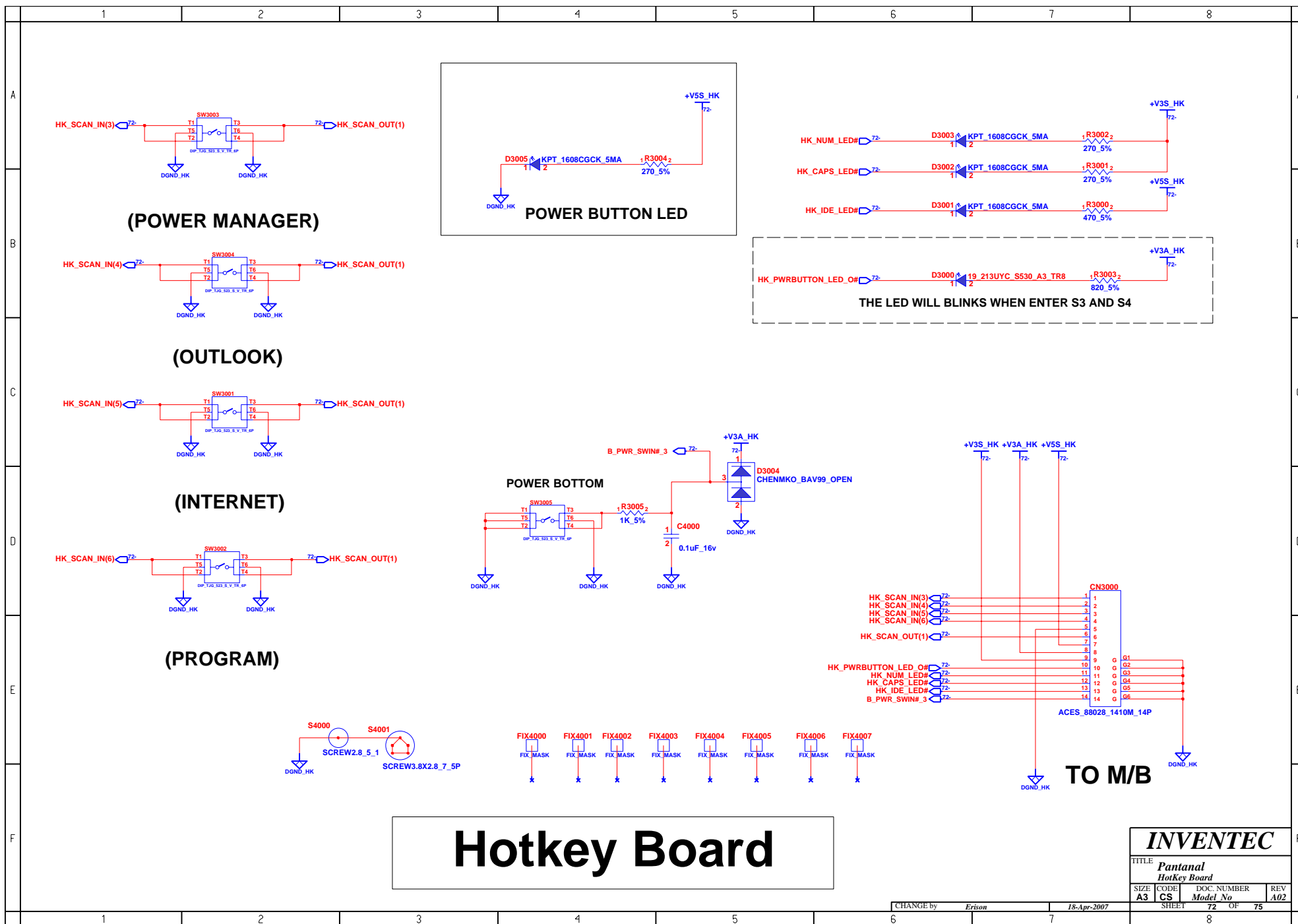
**INVENTEC**

TITLE			
Pantanal			
MB TO OTHER BOARD			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

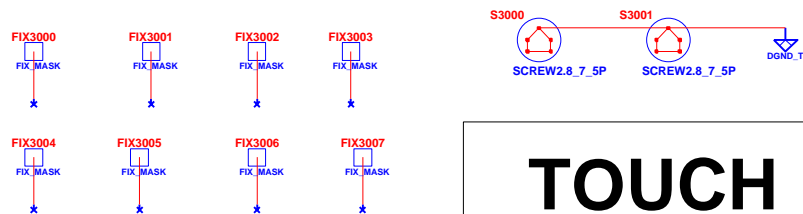
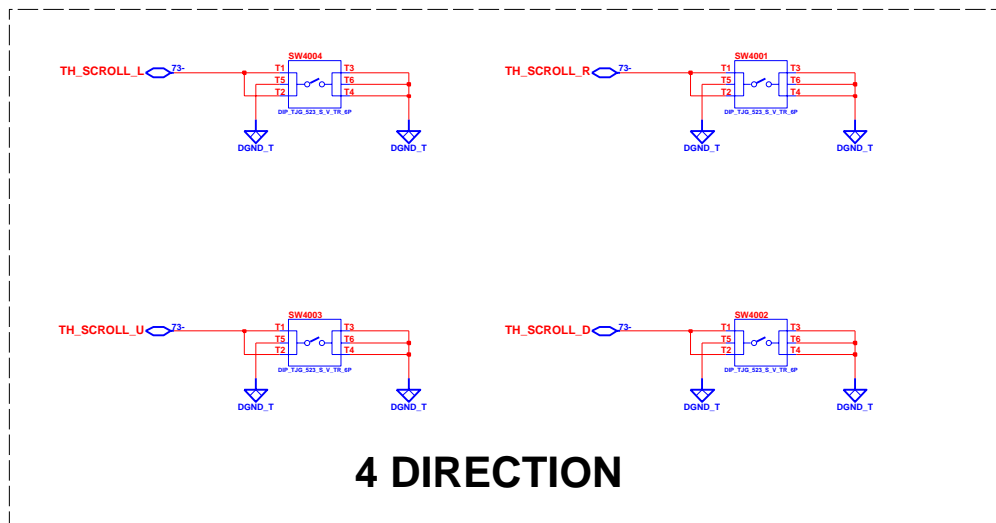
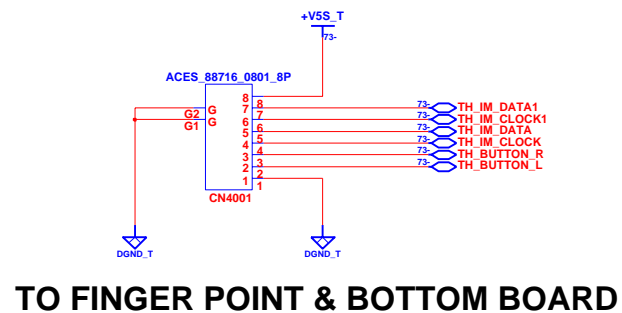
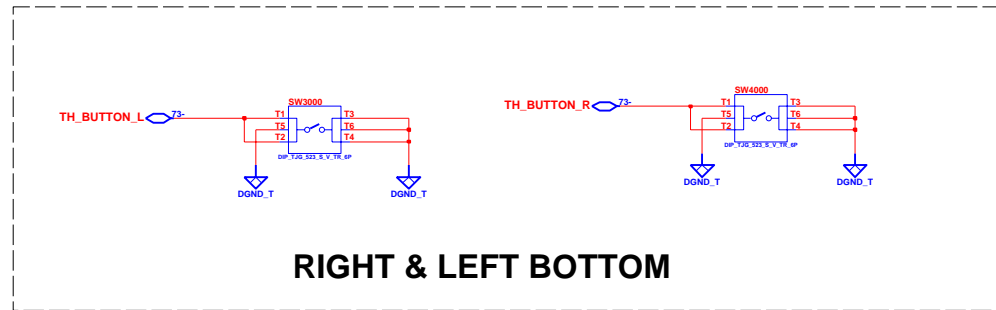
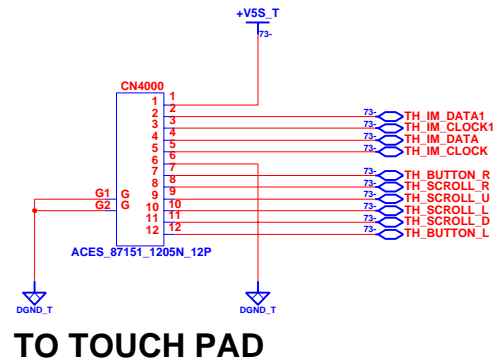
CHANGE by Erison 18-Apr-2007

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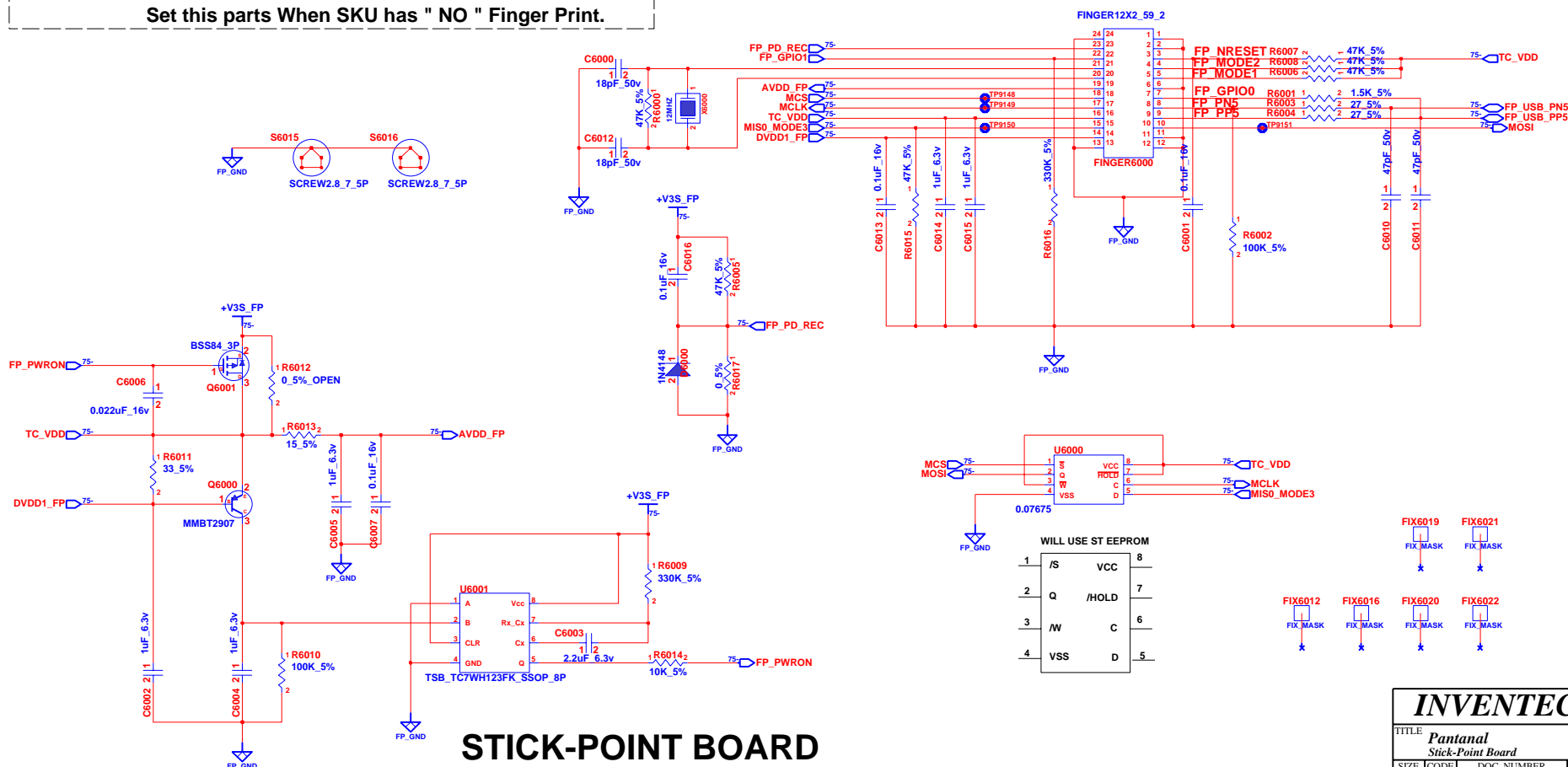
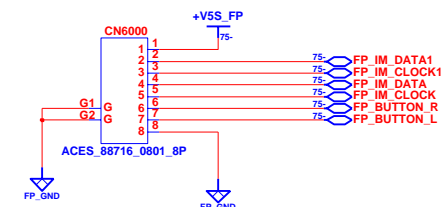
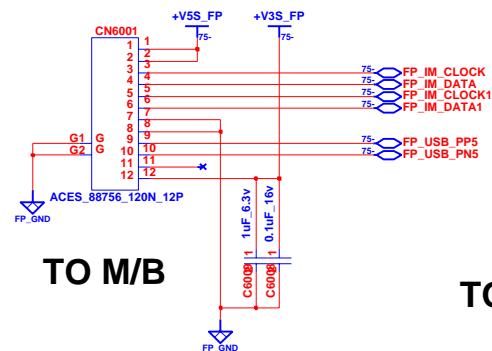
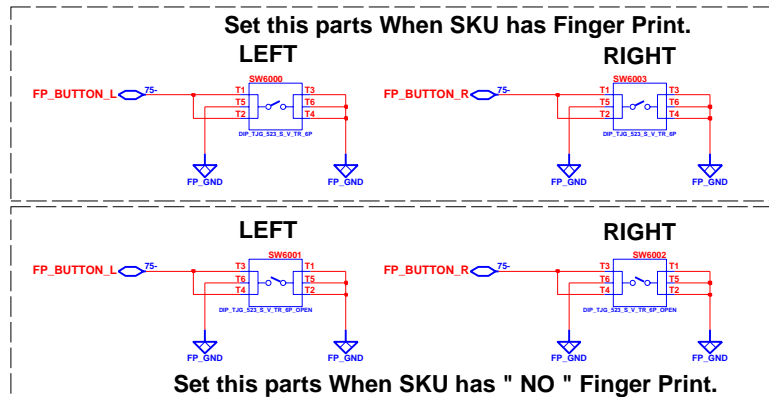


**TOUCH PAD BOTTOM BOARD**

INVENTEC			
TITLE: Pantanal Touch PAD Button Board			
SIZE: A3	CODE: CS	DOC. NUMBER: Model No	REV: A02
SHEET 73		OF 75	

CHANGE by Erison 18-Apr-2007





## STICK-POINT BOARD

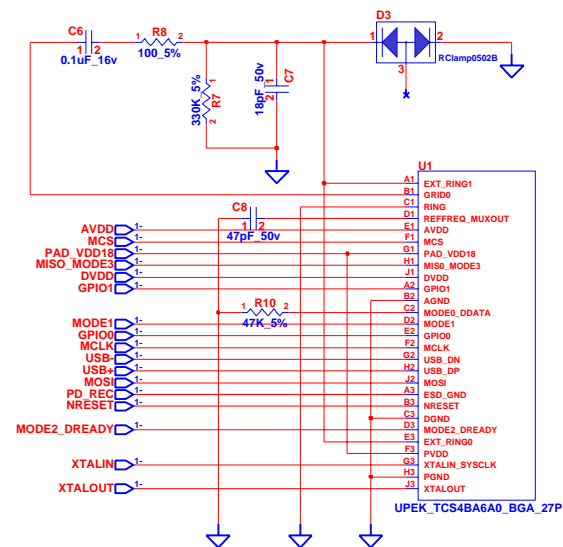
**INVENTEC**

TITLE	<i>Pantanal Stick-Point Board</i>
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SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <i>Model No</i>	REV <b>A02</b>
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CHANGE by	<i>Erison</i>	<i>18-Apr-2007</i>
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CS Model No  
SHEET 75 OF 75



<b><i>INVENTEC</i></b>			
TITLE <b><i>Michigan FINGER PRINT</i></b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <i>Model No</i>	REV <b>000</b>